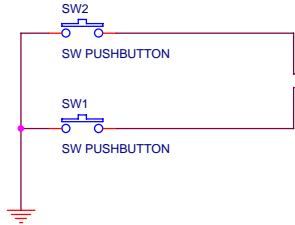
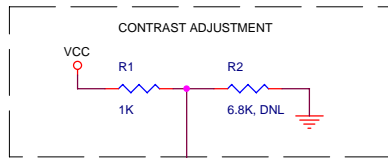


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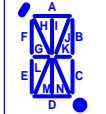
REV	Description
1	ORIGINAL BUILD
2	CORRECTED PINOUT OF J1 AND P1



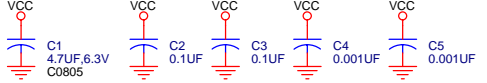
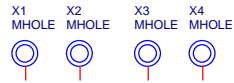
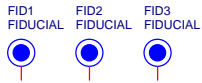
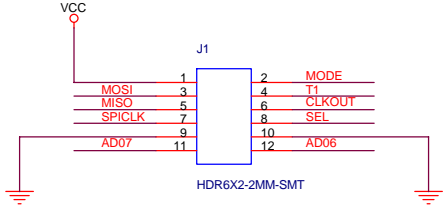
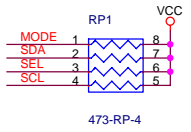
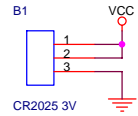
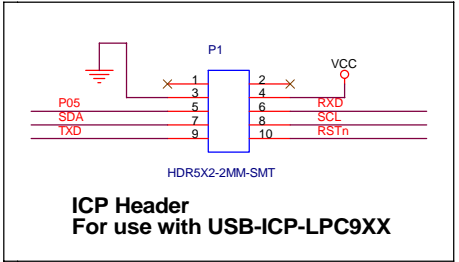
Pin	Signal	Function	Pin	Signal	Function
8	SEL	P0.0/CMP2/KBI0/AD05	S0	S0	
5	MODE	P0.1/CIN2B/KBI1/AD00	S1	S1	
4	MODE	P0.2/CIN2A/KBI2/AD01	S2	S2	
3	SDA	P0.3/CIN1B/KBI3/AD02	S3	S3	
2	SCL	P0.4/CIN1A/KBI4/AD03	S4	S4	
1	P05	P0.5/CMPREF/KBI5	S5	S5	
24	T1	P0.6/CMP1/KBI6	S6	S6	
23	T1	P0.7/T1/KBI7	S7	S7	
22	TXD	P1.0/TXD	S8	S8	
21	RXD	P1.1/RXD	S9	S9	
17	SCL	P1.2/T0/SCL	S10	S10	
16	SDA	P1.3/INT0/SDA	S11	S11	
15	RSTn	P1.4/INT1	S12	S12	
11	RSTn	P1.5/RST	S13	S13	
10	RSTn	P1.6/OCB	S14	S14	
9	RSTn	P1.7/OCC/AD04	S15	S15	
6	AD07	P2.0/ICB/AD07	S16	S16	
7	AD06	P2.1/OCB/AD06	S17	S17	
18	MOSI	P2.2/MOSI	S18	S18	
19	MISO	P2.3/MISO	S19	S19	
20	SPICLK	P2.5/SPICLK	S20	S20	
14	CLKOUT	P3.0/XTAL2/CLKOUT	S21	S21	
13	CLKOUT	P3.1/XTAL1	S22	S22	
27	COM1	BP0	S23	S23	
29	COM2	BP1	S24	S24	
28	COM3	BP2	S25	S25	
30	COM4	BP3	S26	S26	
64	SCL_LCD	SCL_LCD	S27	S27	
63	SDA_LCD	SDA_LCD	S28	S28	
			S29	S29	
			S30	S30	
			S31	S31	

BIT	ADDRESS
7	00
6	00
5	00
4	00
3	00
2	00
1	00
0	00
7	01
6	01
5	01
4	01
3	01
2	01
1	01
0	01
7	02
6	02
5	02
4	02
3	02
2	02
1	02
0	02
7	03
6	03
5	03
4	03
3	03
2	03
1	03
0	03

S29	36	6H/6G/6L/6M
S28	35	6A/6B/6C/6D
S27	34	7A/7B/7C/7D
S26	33	6H/6G/6L/6M
S25	32	6A/6B/6C/6D
S24	31	7A/7B/7C/7D
S23	30	6H/6G/6L/6M
S22	29	6A/6B/6C/6D
S21	28	7A/7B/7C/7D
S20	27	6H/6G/6L/6M
S19	26	6A/6B/6C/6D
S18	25	7A/7B/7C/7D
S17	24	6H/6G/6L/6M
S16	23	6A/6B/6C/6D
S15	22	7A/7B/7C/7D
S14	21	6H/6G/6L/6M
S13	20	6A/6B/6C/6D
S12	19	7A/7B/7C/7D
S11	18	6H/6G/6L/6M
S10	17	6A/6B/6C/6D
S9	16	7A/7B/7C/7D
S8	15	6H/6G/6L/6M
S7	14	6A/6B/6C/6D
S6	13	7A/7B/7C/7D
S5	12	6H/6G/6L/6M
S4	11	6A/6B/6C/6D
S3	10	7A/7B/7C/7D
S2	9	6H/6G/6L/6M
S1	8	6A/6B/6C/6D
S0	7	7A/7B/7C/7D



LCD1
LCD-8CHAR, PD-878



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