

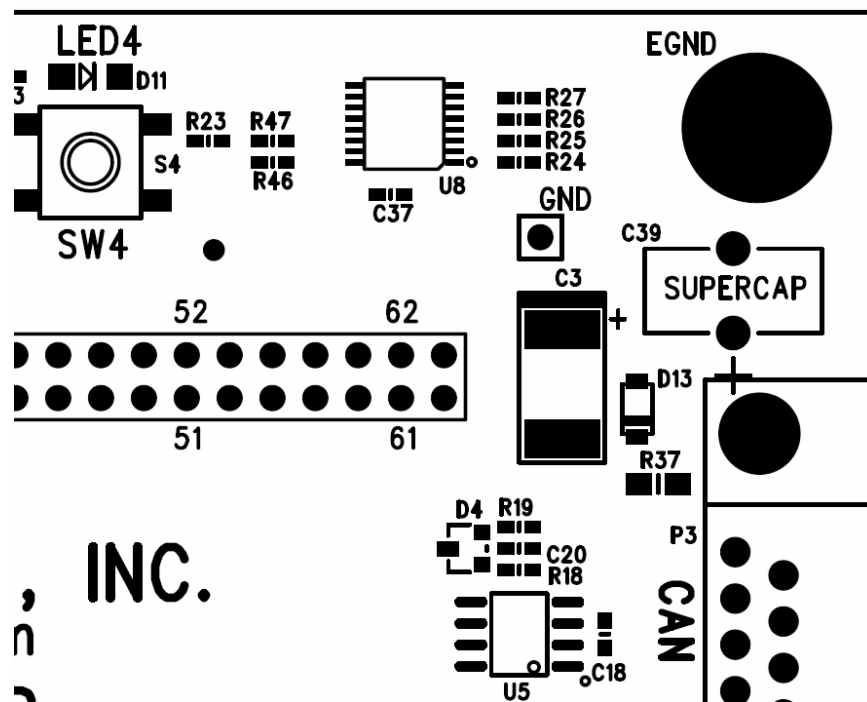
FDI	ENGINEERING CHANGE ORDER		ECO NO: 09-008
			Page 1 of 1
TITLE	DWG NO.	CURRENT REV	NEW REV
ARMCARRIER Board	ARMCARRIER	1.1	1.2

BACKGROUND:

The RTC Supercap Backup circuitry utilizes a reverse protection diode and current limiting resistor. The diode selected has a high reverse leakage current which results in short backup duration. The current limiting resistor requires the power to be applied for abnormally long time for proper charge on the super cap.

DESCRIPTION OF CHANGE:

1. Change D13 from PN: MBR120 to PN: BAS416, FDI PN: D500066
2. Change R37 from 330 ohm to 33 ohm, FDI PN: R500005



Reason for change: 1. Change diode to low leakage 2. Change resistor to small value for quicker charge-up		Disposition/Effectively	
		Use as is	
		Rework to ECO	X
		Scrap & Rebuild	
		Record Change Only	
Prepared by:	M. Hall	Date:	05/19/09
Approved by:		Date:	
Approved by:		Date:	
Approved by:		Date:	

FDI	ENGINEERING CHANGE ORDER		ECO NO: 09-014
			Page 1 of 4
TITLE	DWG NO.	CURRENT REV	NEW REV
ARMCARRIER		1.2	1.3

BACKGROUND:

To rework the ARMCARRIER PCB in order to provide unique chip select for the SPI interface to the LCD Carrier and connect the I2C bus to the LCD Carrier.

The SPI chip select is required for the 3.5" LCD modules. This is connected to signal 'GPIO36'.

- For ARM7DIMM this is connected to P2.11
- For ARM9DIMM this is connected to GPO_17

The I2C bus is a future feature to provide 'plug-n-play' capability of the LCD Carrier to allow the software to automatically recognize the different LCD without re-programming the firmware.

Reason for change: 1. Provide chip select to the LCD Carrier for using the SPI interface with the Okaya 3.5" LCD 2. Add I2C connectivity to the LCD Carrier			Disposition/Affectivity	
			Use as is	
			Rework to ECO	x
			Scrap & Rebuild	
			Record Change Only	
Prepared by:	JG	Date:	07/29/2009	
Approved by:		Date:		
Approved by:		Date:		
Approved by:		Date:		

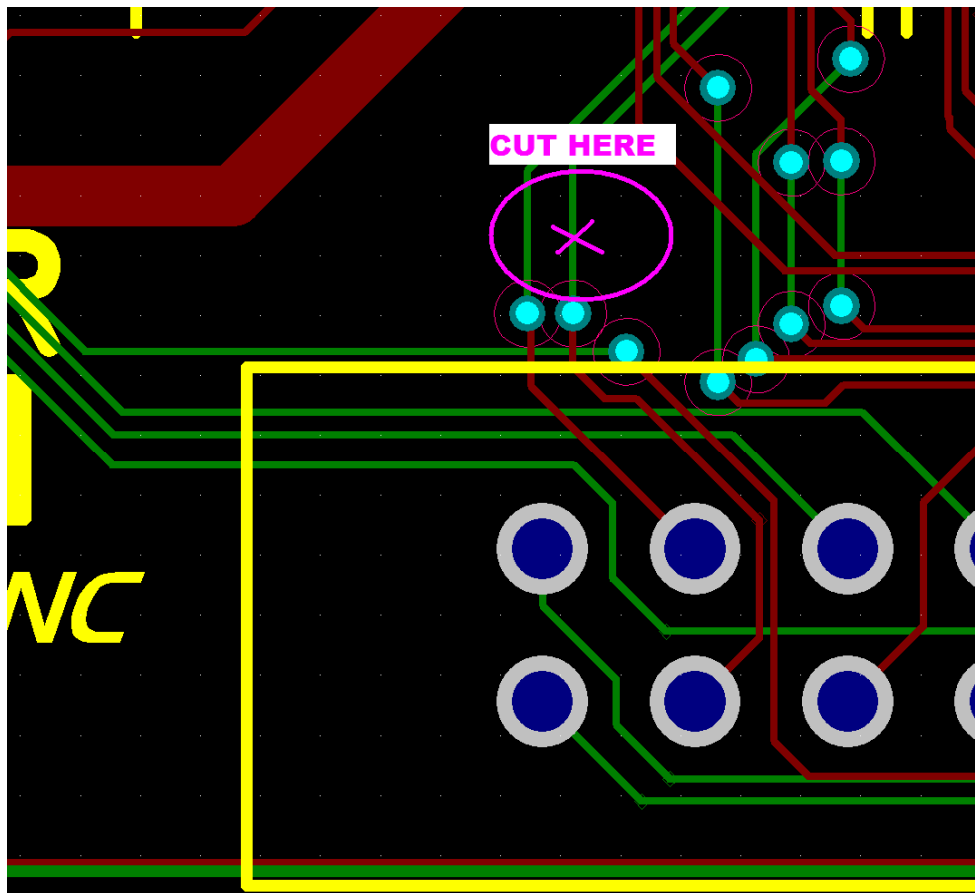
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TITLE	DWG NO.	CURRENT REV	NEW REV
ARMCARRIER		1.2	1.3

DESCRIPTION OF CHANGE:

CUT:

On the bottom side, cut the trace coming from the via connected to J7 pin 48

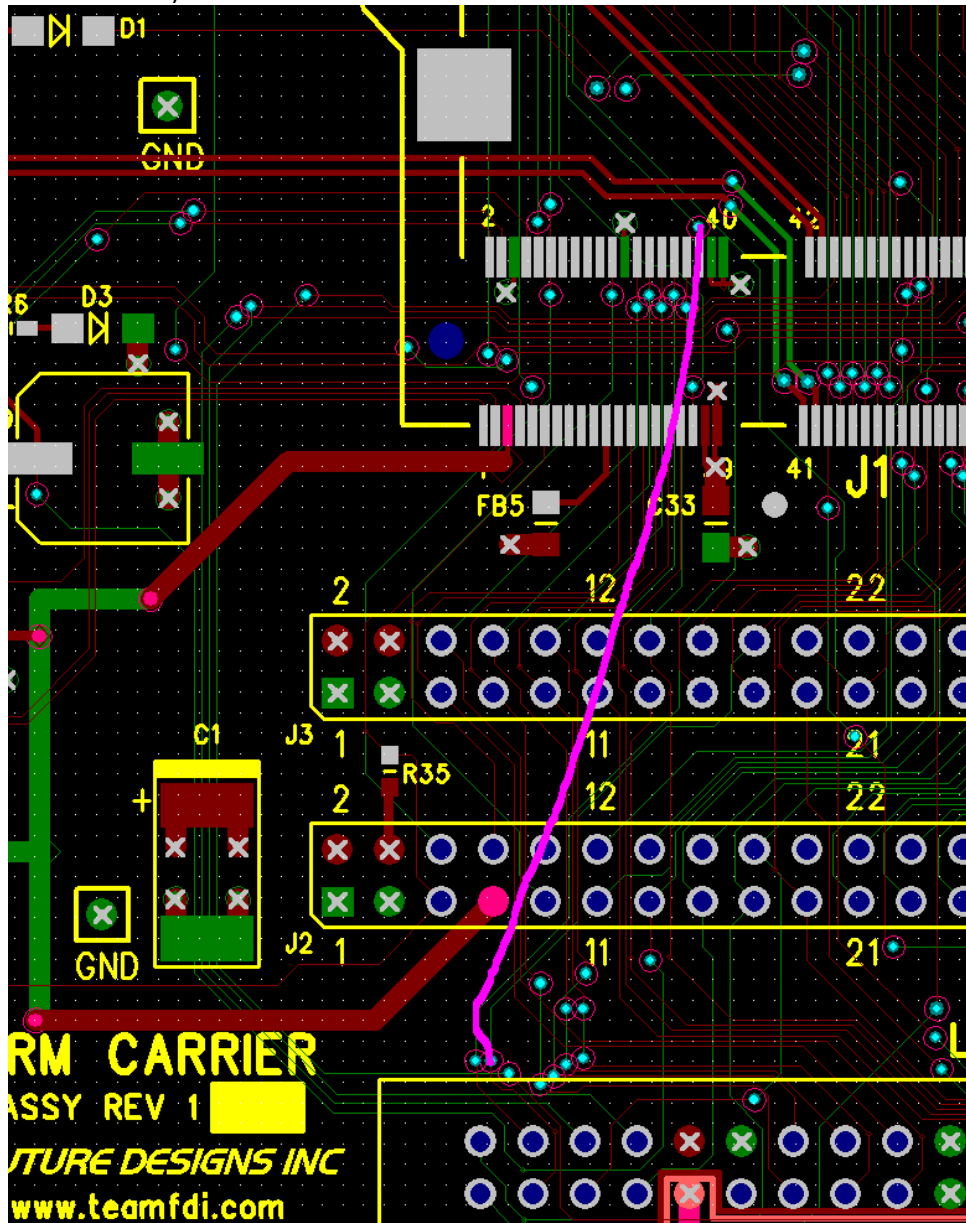
(see illustration below)



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TITLE	DWG NO.	CURRENT REV	NEW REV
ARMCARRIER		1.2	1.3

JUMPER(S):

- 1) On the bottom side, add a jumper from the via previously cut (J7 pin 48) to J1 pin 36 (see illustration below)



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TITLE	DWG NO.	CURRENT REV	NEW REV
ARMCARRIER		1.2	1.3

2) On the bottom side, add a jumper from via adjacent to U10 pin 5 to J7 pin 14 (SDA)

3) On the bottom side, add a jumper from via adjacent to U10 pin 6 to J7 pin 13 (SCL)

(see illustration below for both jumpers)

