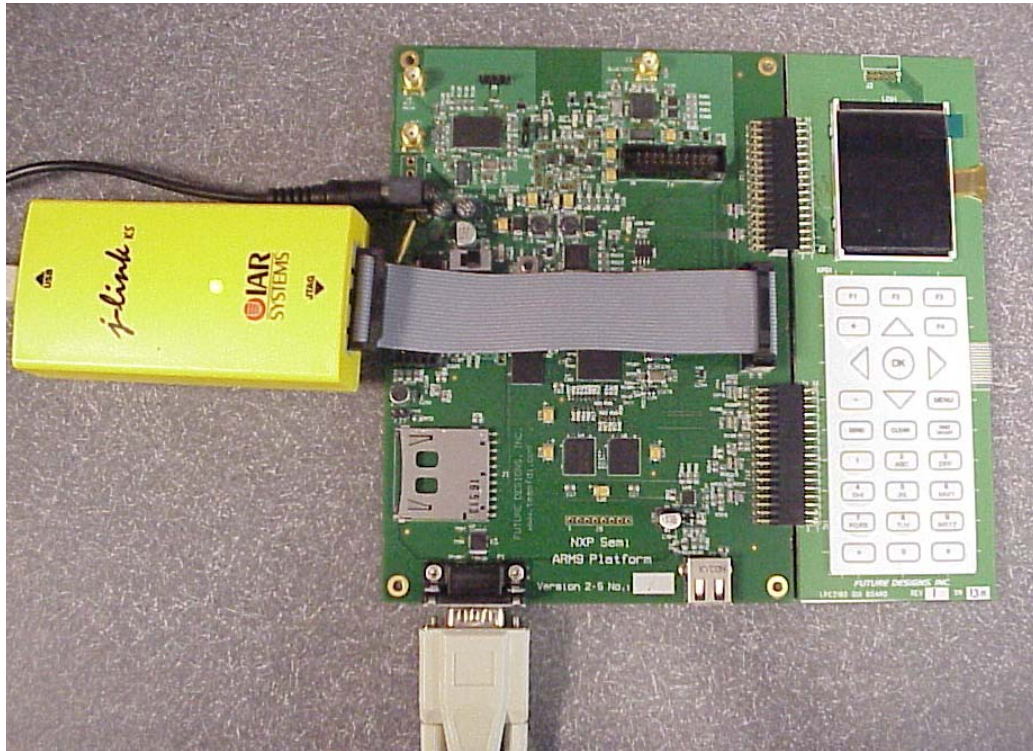


Objective

The Program SIBL Utility can be used to refresh the boot loader of the FDI LPC3180-DEV-KIT Board using a USB to JTAG J-Link-ARM-KS device.

Steps to Flash

- 1) Install J-Link tools from http://www.segger.com/download_jlink.html if not already done.
- 2) Download “Program SIBL.zip” and unzip.
- 3) Edit file “Program SIBL.bat” and point to where J-Link Commander was installed.
- 4) Connect J-Link to J2 of the LPC3180 and a serial port on P1 and open terminal running at 115,200 baud, 8 bits, no parity, 1 stop bit, and no hardware flow control.



- 5) Double click on file “Program SIBL.bat” to run J-Link Commander with a script to perform the flashing.



LPC3180 Program SIBL Utility



- 6) Out of the serial port will appear the following information:

```
PrgSIBL (Jan 5 2007)
Programming SIBL:
Erasing block 0 ... OK
Writing page 0 ... OK
Writing page 1 ... OK
Writing page 2 ... OK
...
Writing page 15 ... OK
Writing page 16 ... OK

Verifying SIBL:
Verifying page 0 ...
Verifying page 1 ...
Verifying page 2 ...
...
Verifying page 15 ...
Verifying page 16 ...
**PASS**
Reset the board to start
```

- 7) You are now complete. Reset the LPC3180 board and the following lines will appear out of the serial port.

```
43210
LCD Init Status: 00C8NXP LPC3180 Demo
FDI SIBL v1.00
```