

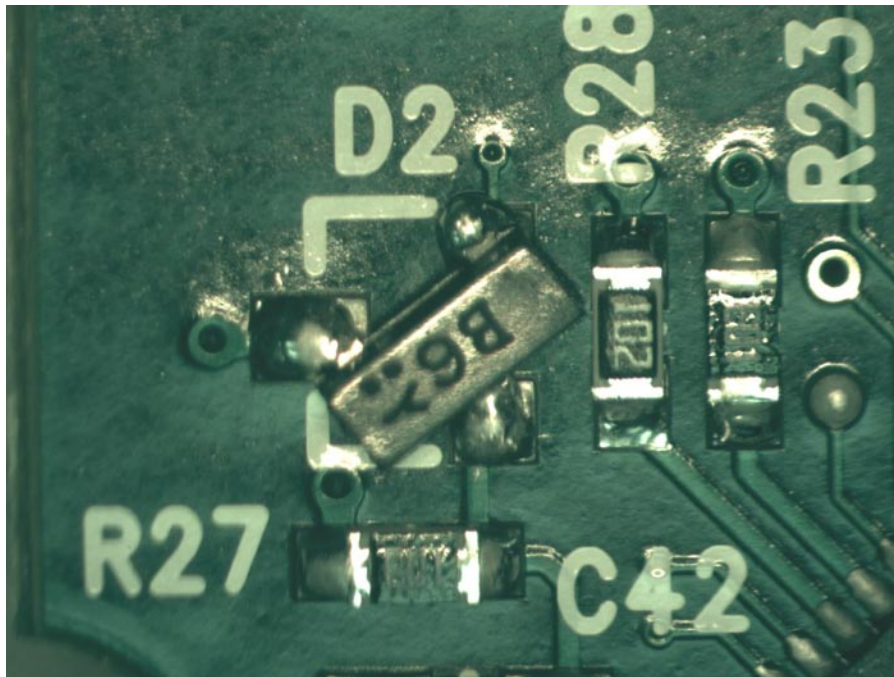
FDI	ENGINEERING CHANGE ORDER		ECO NO: 09-005b
			Page 1 of 2
TITLE	DWG NO.	CURRENT REV	NEW REV
ARM9DIMM LP3250	ARM9DIMM	1.0	1.1

BACKGROUND:

- The Ethernet PHY is intermittent and would powerup at different addresses. It was determined that the reset circuit was not functioning properly due to improper footprint for D2.
- The CPU core voltage needs to be increased to 1.35V in order to support 266MHz.
- The Wall Power Detect needs to be disconnected from the LPC3250 to allow the touch screen to function properly.
- The Seggar JTAG Debugger utilizes a IDC 10-pin 2mm ribbon connector that will not work with the original shrouded connector
- The Debug Connector is required for initial programming

DESCRIPTION OF CHANGE:

1. Rotate D2 diode 120 degrees counter-clockwise

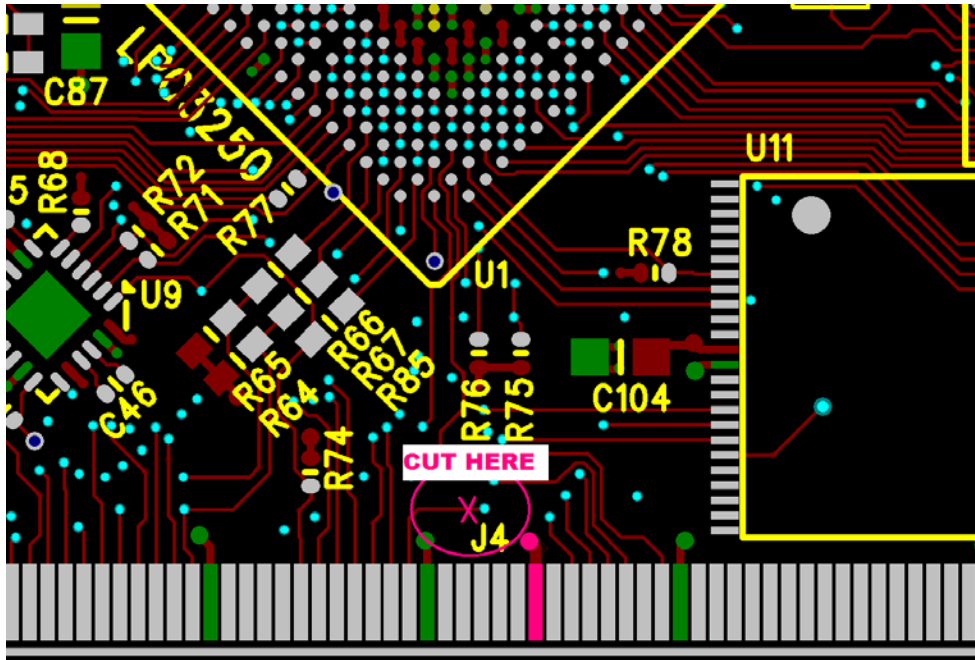


2. Change the following resistor values:
 - a. Change R119 from 5.49K, 1% to 3.65K, 1%, 0603
 - b. Change R120 from 10.0K, 1% to 4.99K, 1%, 0603
 - c. Change R122 from 24.0K, 1% to 17.4K, 1%, 0603

Reason for change: <ol style="list-style-type: none"> 1. Correction of SOT23 footprint 2. Vcore increase to 1.35V 3. Isolate Wall Detect from TS AD input 4. Change to un-shoruded connector for Seggar JTAG 5. Load J1, FDI PN: J500030 		Disposition/Effectively	
		Use as is	
		Rework to ECO	X
		Scrap & Rebuild	
		Record Change Only	
Prepared by:	M. Hall	Date:	05/08/09
Approved by:		Date:	
Approved by:		Date:	
Approved by:		Date:	

FDI	ENGINEERING CHANGE ORDER		ECO NO: 09-005b
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ARM9DIMM LP3250	ARM9DIMM	1.0	1.1

3. On the top side, cut the trace



4. Change J3 from Hirose DF11 shrouded connector (J000168) to Norcomp 2mm un-shrouded right-angle 2mm 5x2 header, FDI PN: J100036
5. Change J1 to "LOAD", FDI PN: J500030 , Qty = 1

Reason for change: <ol style="list-style-type: none"> 1. Correction of SOT23 footprint 2. Vcore increase to 1.35V 3. Isolate Wall Detect from TS AD input 4. Change to un-shoruded connector for Seggar JTAG 5. Load J1, FDI PN: J500030 		Disposition/Effectively	
		Use as is	
		Rework to ECO	X
		Scrap & Rebuild	
		Record Change Only	
Prepared by:	M. Hall	Date:	05/08/09
Approved by:		Date:	
Approved by:		Date:	
Approved by:		Date:	

FDI	ENGINEERING CHANGE ORDER		ECO NO: 09-010
			Page 1 of 1
TITLE	DWG NO.	CURRENT REV	NEW REV
ARM9DIMM LP3250	ARM9DIMM	1.1	1.2

BACKGROUND:

- During Linux boot, memory errors occur. The Vcore_var regulator is not regulating properly. The presence of C64 (0.01UF) on the feedback pin of the regulator causes improper output regulation.

DESCRIPTION OF CHANGE:

1. Remove C64 completely – do not load anything on location C64.

Reason for change: 1. To correct issue with V_core_adj regulator		Disposition/Effectively	
		Use as is	
		Rework to ECO	X
		Scrap & Rebuild	
		Record Change Only	
Prepared by:	M. Hall	Date:	06/12/09
Approved by:		Date:	
Approved by:		Date:	
Approved by:		Date:	