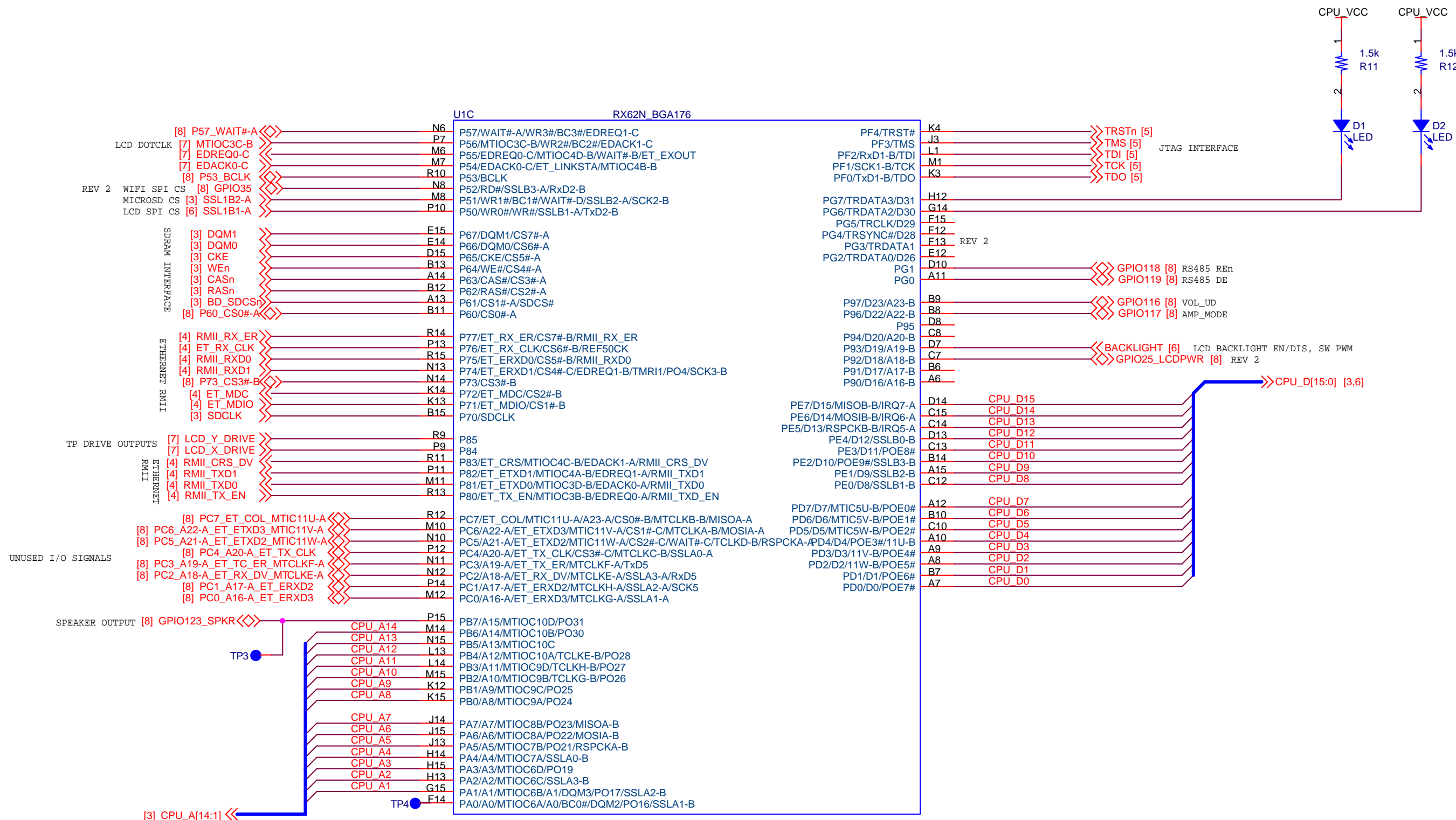
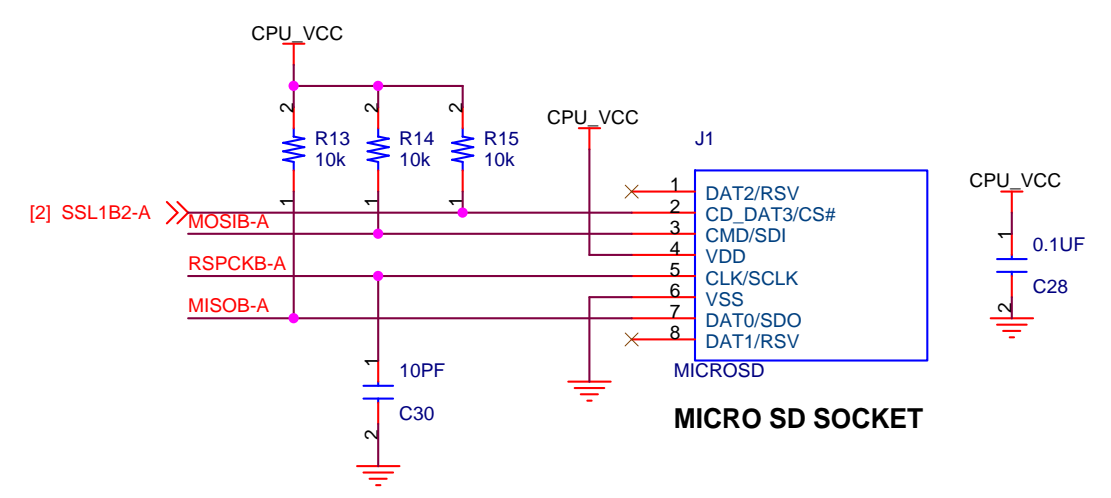
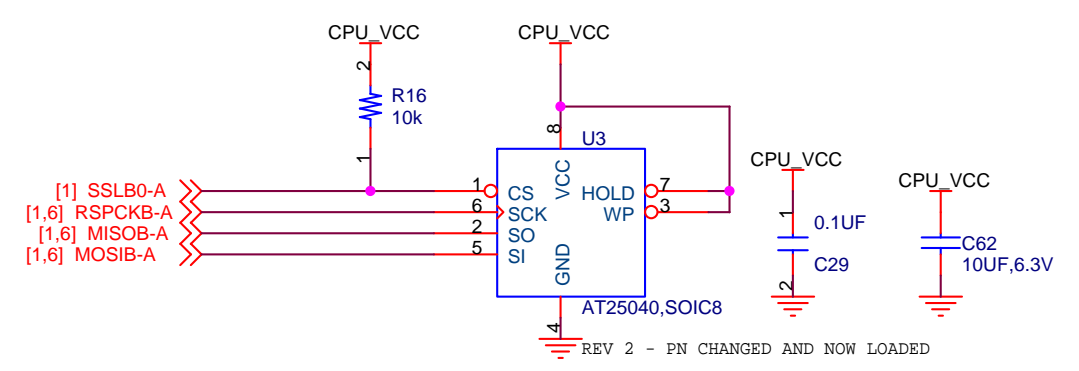
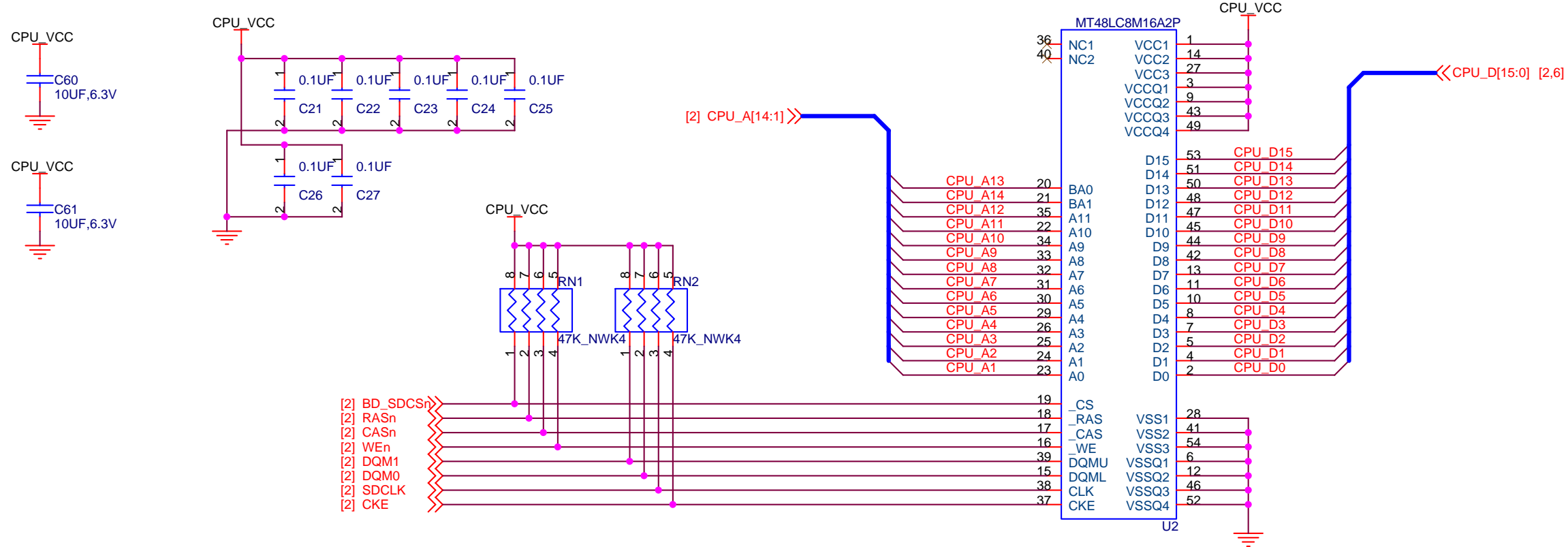


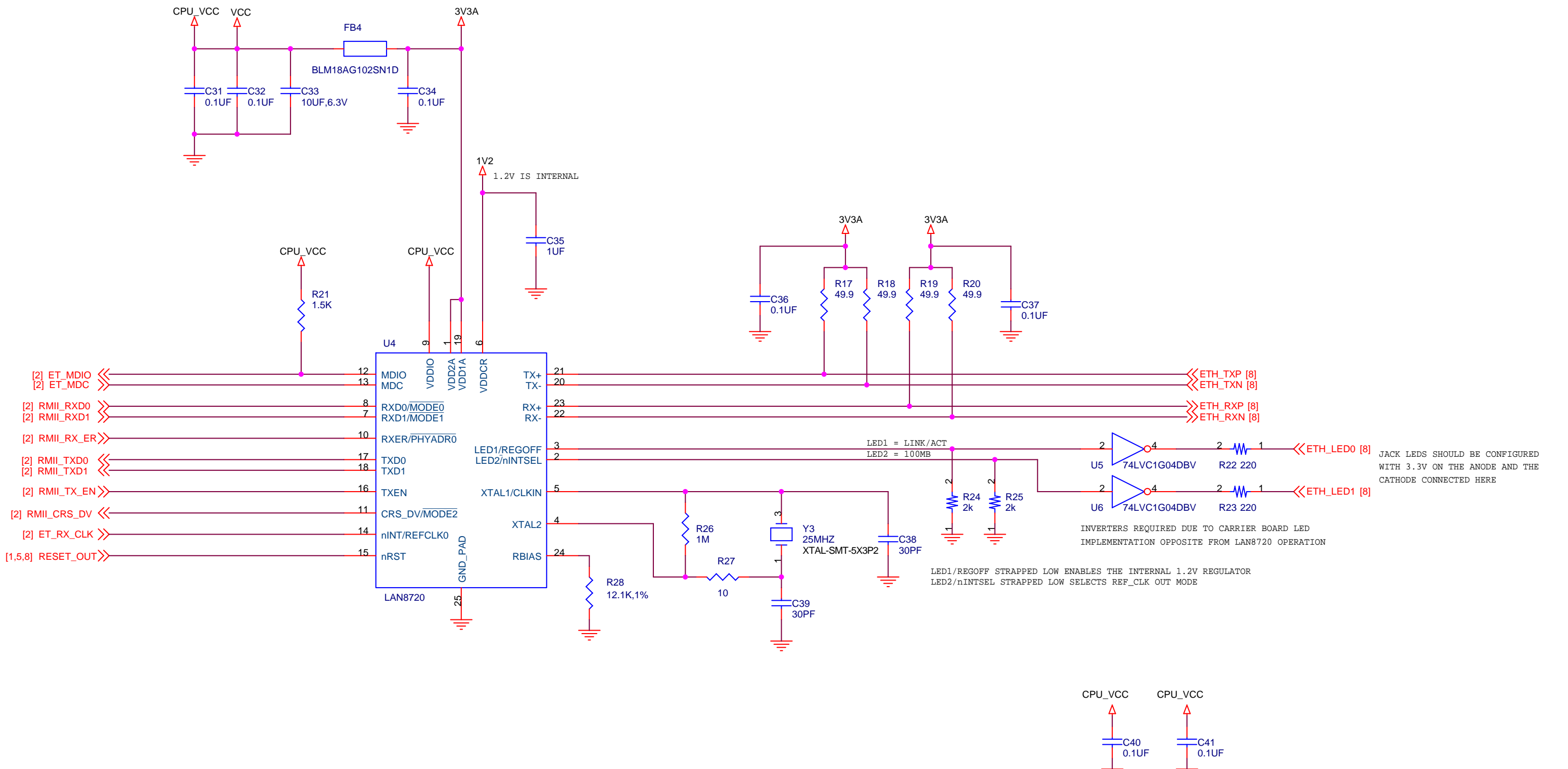
FUTURE DESIGNS, INC.	
Title	SOMDIMM-RX62N
Size	Document Number P:\DK-TS-KIT\SOMDIMM-RX62N\SCH\SOMDIMM-RX62N_SCH_2.DSN
Date:	Monday, August 08, 2011
Sheet	1 of 8
Rev	2



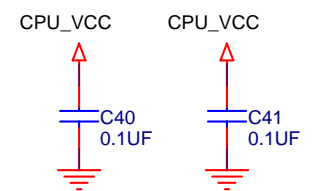
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Title CPU MEMORY IO JTAG		
Size B	Document Number P:\DK-TS-KIT\SOMDIMM-RX62N\SCH\SOMDIMM-RX62N_SCH_2.DSN	Rev 2
Date: Monday, August 08, 2011	Sheet 2	of 8



FUTURE DESIGNS, INC.		
Title SDRAM MEMORY EEPROM MICROSD		
Size B	Document Number P:\DK-TS-KIT\SOMDIMM-RX62N\SCH\SOMDIMM-RX62N_SCH_2.DSN	Rev 2
Date: Monday, August 01, 2011	Sheet 3	of 8



ETHERNET



LED1 = LINK/ACT
LED2 = 100MB

JACK LEDs SHOULD BE CONFIGURED WITH 3.3V ON THE ANODE AND THE CATHODE CONNECTED HERE

INVERTERS REQUIRED DUE TO CARRIER BOARD LED IMPLEMENTATION OPPOSITE FROM LAN8720 OPERATION

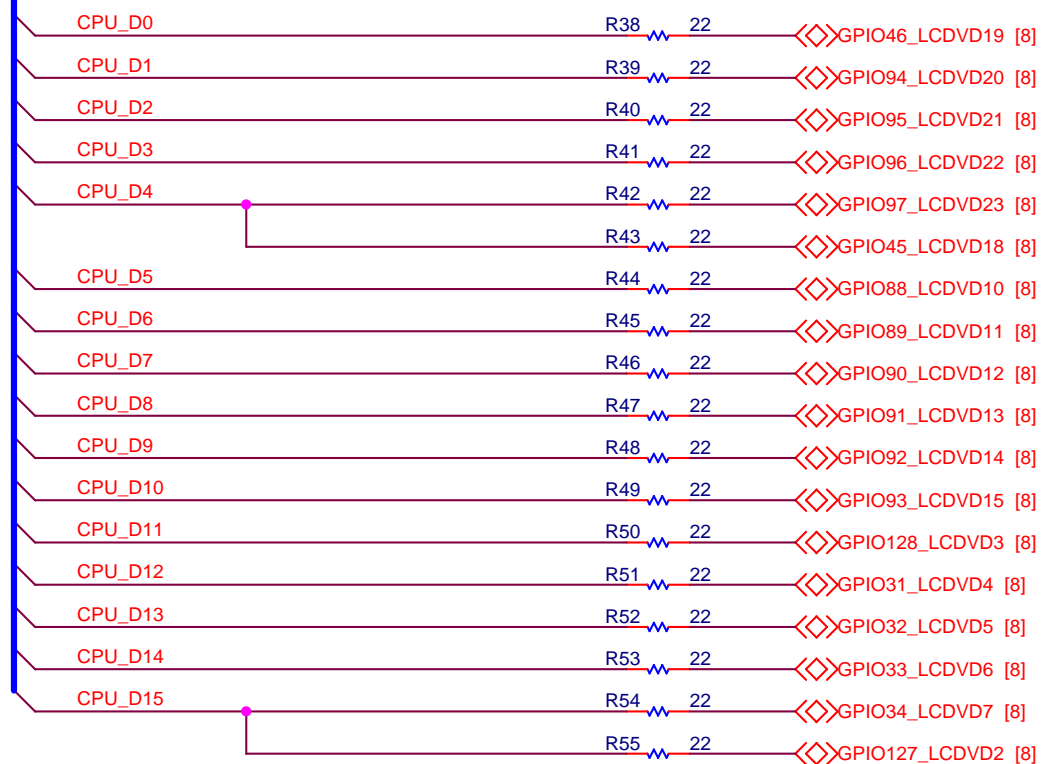
LED1/REGOFF STRAPPED LOW ENABLES THE INTERNAL 1.2V REGULATOR
LED2/nINTSEL STRAPPED LOW SELECTS REF_CLK OUT MODE

FUTURE DESIGNS, INC.			
Title ETHERNET			
Size B	Document Number P:\DK-TS-KIT\SOMDIMM-RX62N\SCH\SOMDIMM-RX62N_SCH_2.DSN		Rev 2
Date:	Monday, August 01, 2011	Sheet 4	of 8

[2,3] CPU_D[15:0]

TO RX62N

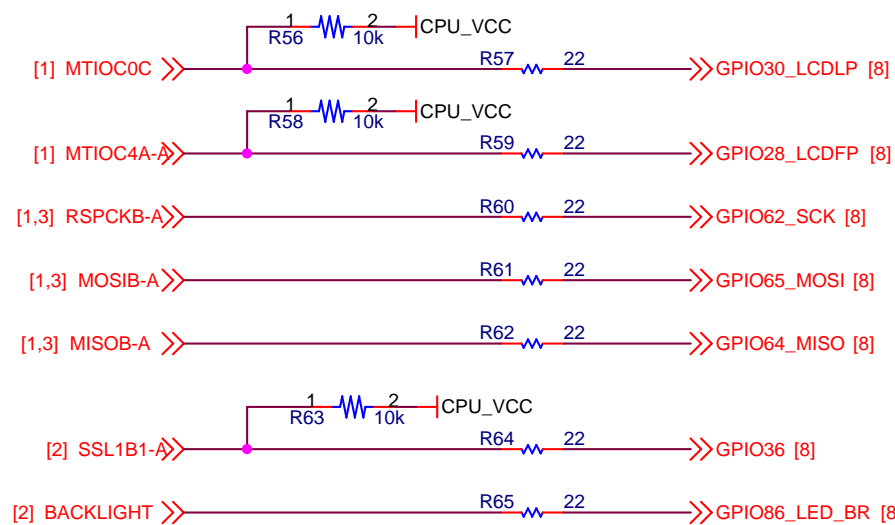
TO FDI CARRIER



CPU_D0 = BLUE1 = LCDVD19
 |
 CPU_D4 = BLUE5 = LCDVD23
 LCDVD18 = LCDVD23

 CPU_D5 = GREEN0 = LCDVD10
 |
 CPU_D10 = GREEN5 = LCDVD15

 CPU_D11 = RED1 = LCDVD3
 |
 CPU_D15 = RED5 = LCDVD7
 LCDVD2 = LCDVD7



HORIZONTAL SYNC

VERTICAL SYNC

SPI CLOCK

SPI MASTER DATA OUT

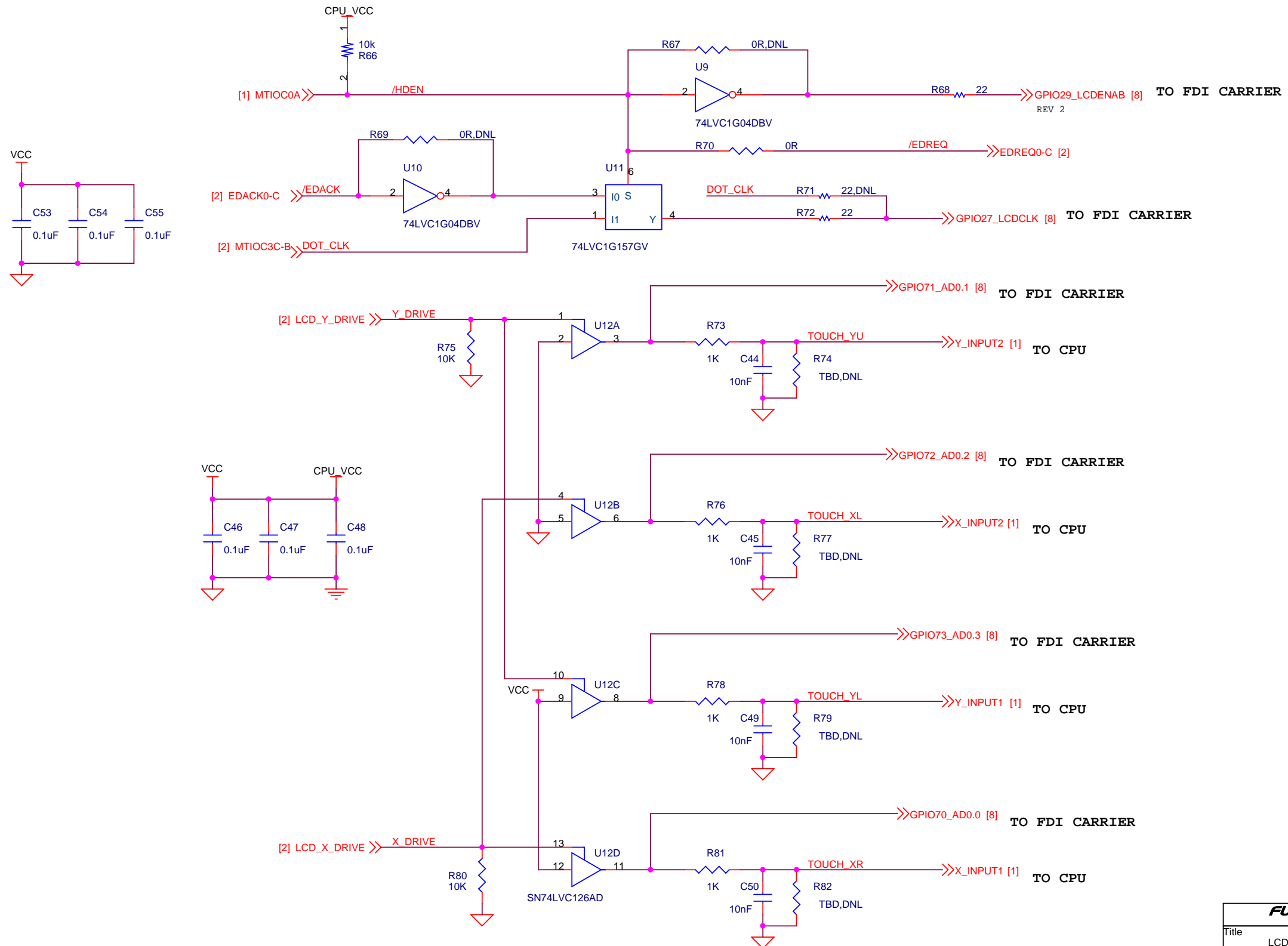
SPI MASTER DATA IN

SPI CHIP SELECT

BACKLIGHT PWM

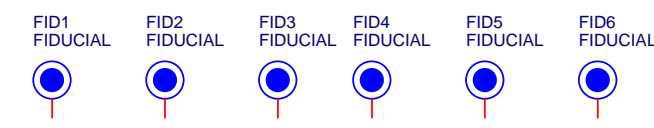
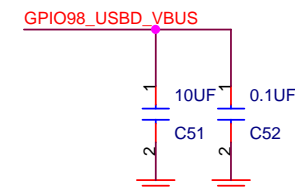
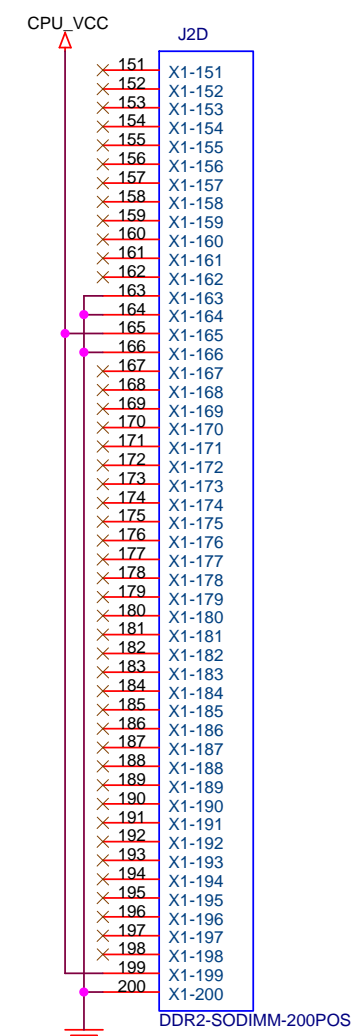
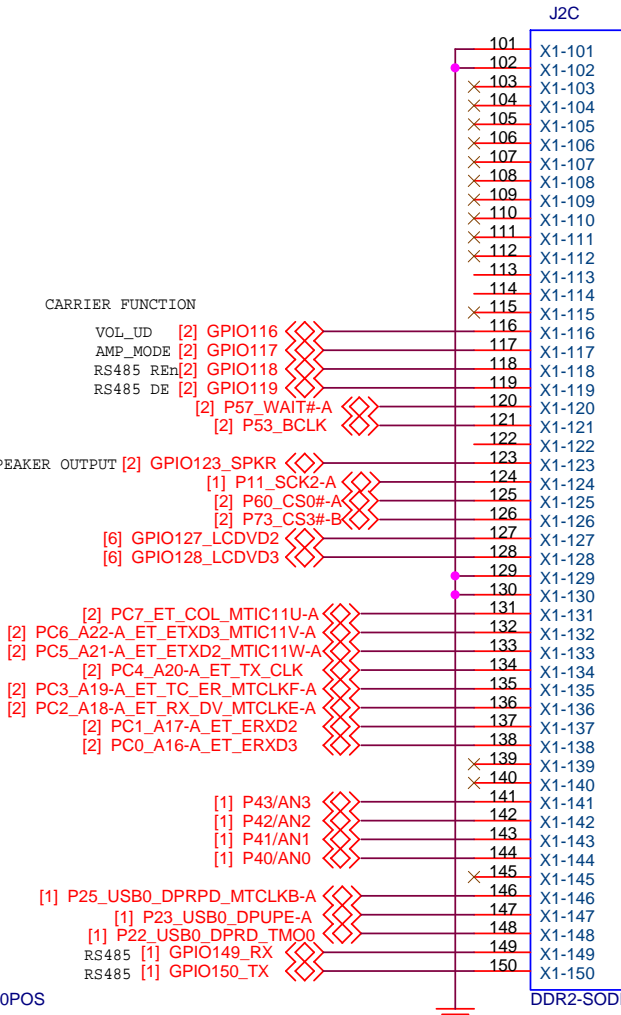
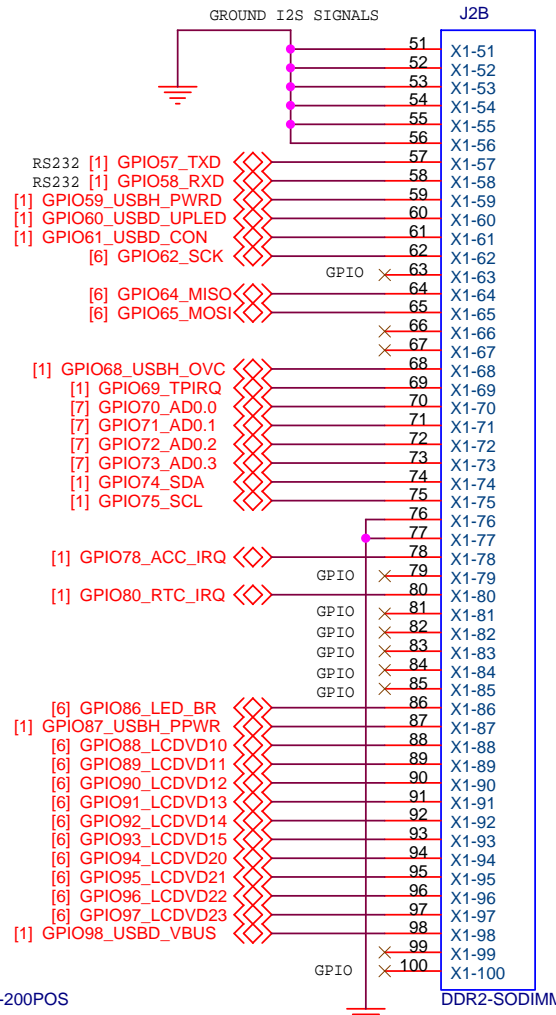
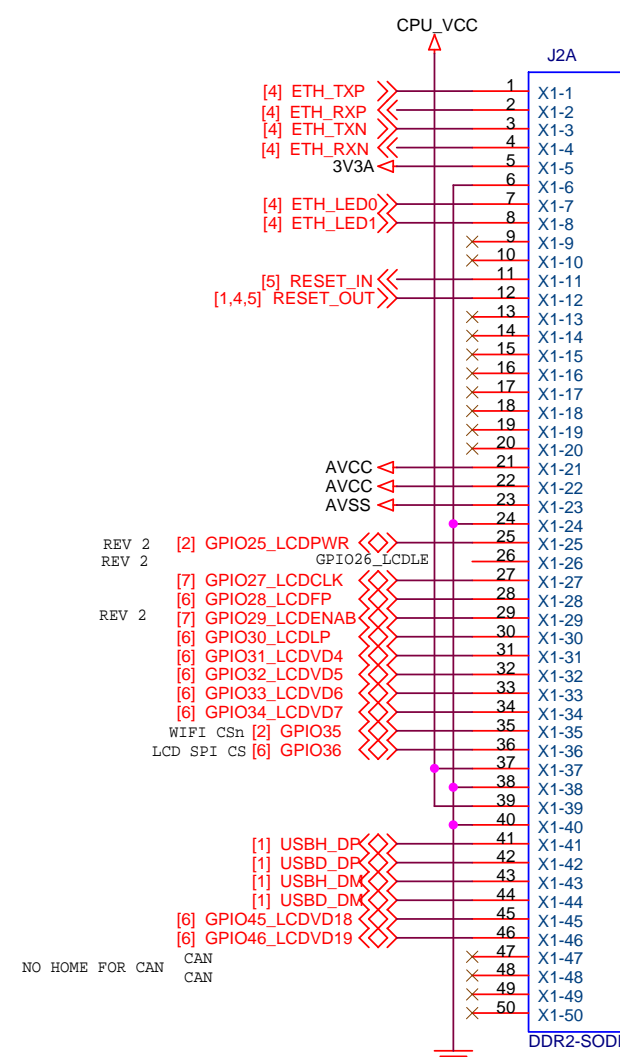
FUTURE DESIGNS, INC.

Title		
LCD INTERFACE		
Size	Document Number	Rev
B	P:\DK-TS-KIT\SOMDIMM-RX62N\SCH\SOMDIMM-RX62N_SCH_2.DSN	2
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REV 2 - SPLIT THE CPU<>TP SIGNALS

FUTURE DESIGNS, INC.		
Title LCD TIMING GENERATION & TOUCH INTERFACE		
Size	Document Number P:\DK-TS-KIT\SOMDIMM-RX62N\SCH\SOMDIMM-RX62N_SCH_2.DSN	Rev 2
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FUTURE DESIGNS, INC.	
Title: SODIMM EDGE FINGERS	
Size: Document Number	Rev 2
P:\DK-TS-KIT\SODIMM-RX62N\SCH\SODIMM-RX62N_SCH_2.DSN	
Date: Monday, August 01, 2011	Sheet 8 of 8