



uEZ GUI 7.0 Hardware Revision History



September 2, 2011

Revision 1.0 Original Release

Revision 1.1 (ECO 10-064)

- Two Touch Panel GPIO signals were moved to different pins on the CPU that have push-pull drive capability as needed.
 - U1 pin 50 was connected to U1 pin 67
 - Up pin 48 was connected to U1 pin 66

Revision 1.2 (ECO 10-065)

- A P-channel FET to control power to the LCD was added
 - The following traces to power vias were cut: C68, C69, C70 (all sides closest to J7), J7 pin 4,5,6, & 7
 - FET PN: FDN358P was added adjacent to C69 up side down with the drain connected to the via and the source connected to C69
 - A wire was added to connect the following pins together in a bug: J7 pin 7, 6, 5, & 4 to C69+, C68+, & C70+
 - A wire was added from the gate pin of the added FET (FDN358P) to U1 pin 193

Revision 2.0 (ECO 10-068)

- Previous rework of ECO's 10-064 and 10-065 were implemented
- An optional 2 pin SMT header J9 was added for off-board speaker support. This was connected to speaker signals U17 pins 8 & 5
- An optional 3 pin SMT header J10 and a 330ohm 1/4W series resistor, R51, were added for off-board battery support. A 3.6V Zener Diode D4 was added and diode D5 was changed from a BAS416 to BAV74 as depicted in ECO 10-068
- Optional USB Host support circuitry was added to support on-board USB connector (for use with Mini-AB and adapter cable) as shown in ECO 10-068

Revision 2.1 (ECO 11-091)

- Switched the optional off-board battery backup connector (J10) and associated resistor (R51) from DNL to load to match configuration of Harvard Apparatus.
- Future assemblies will be marked as Rev 2.1 during manufacturing