

REVISION HISTORY		
01/27/10	1	INITIAL RELEASE
5/3/10	1.1	MOVE SPEAKER OUTPUT, ADD CAP, REWORK SUPERCAP

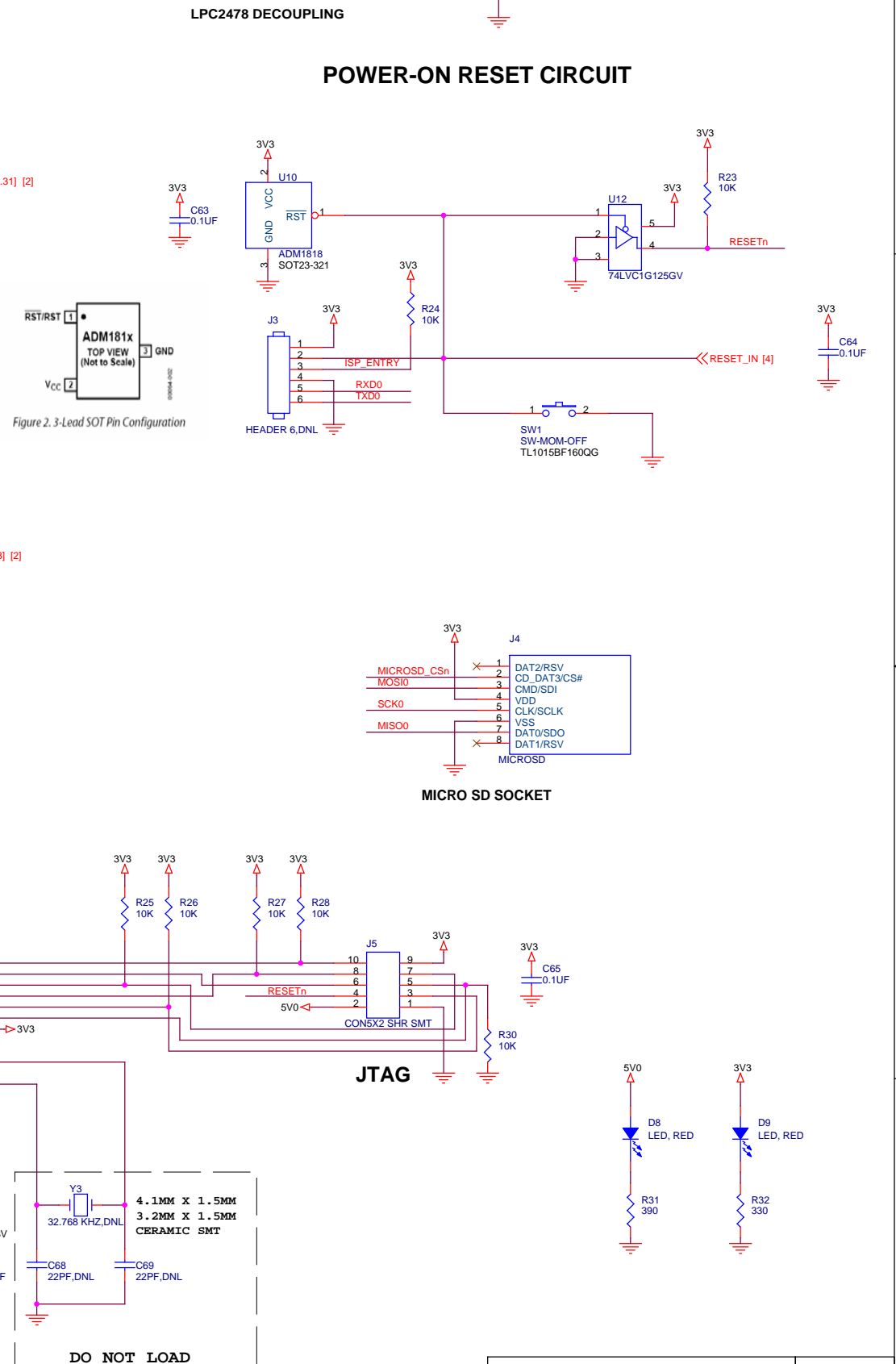
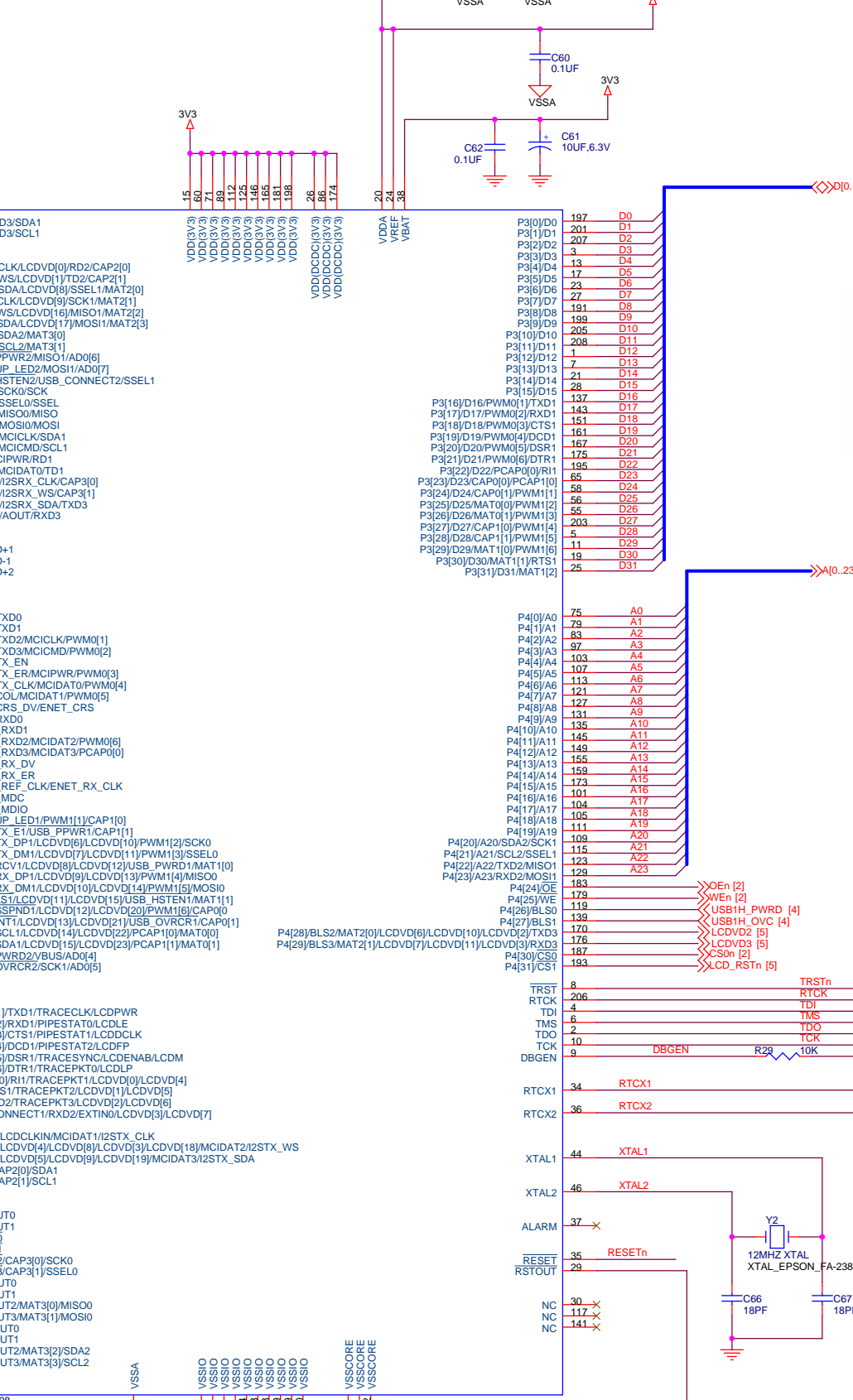
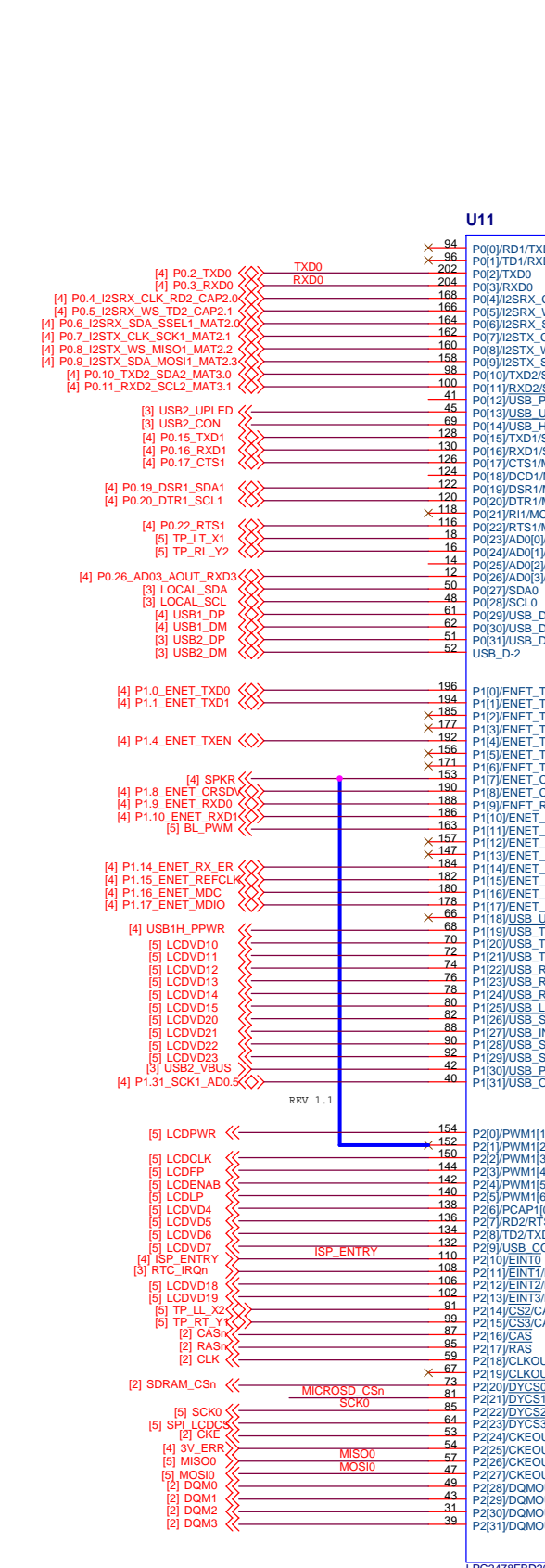
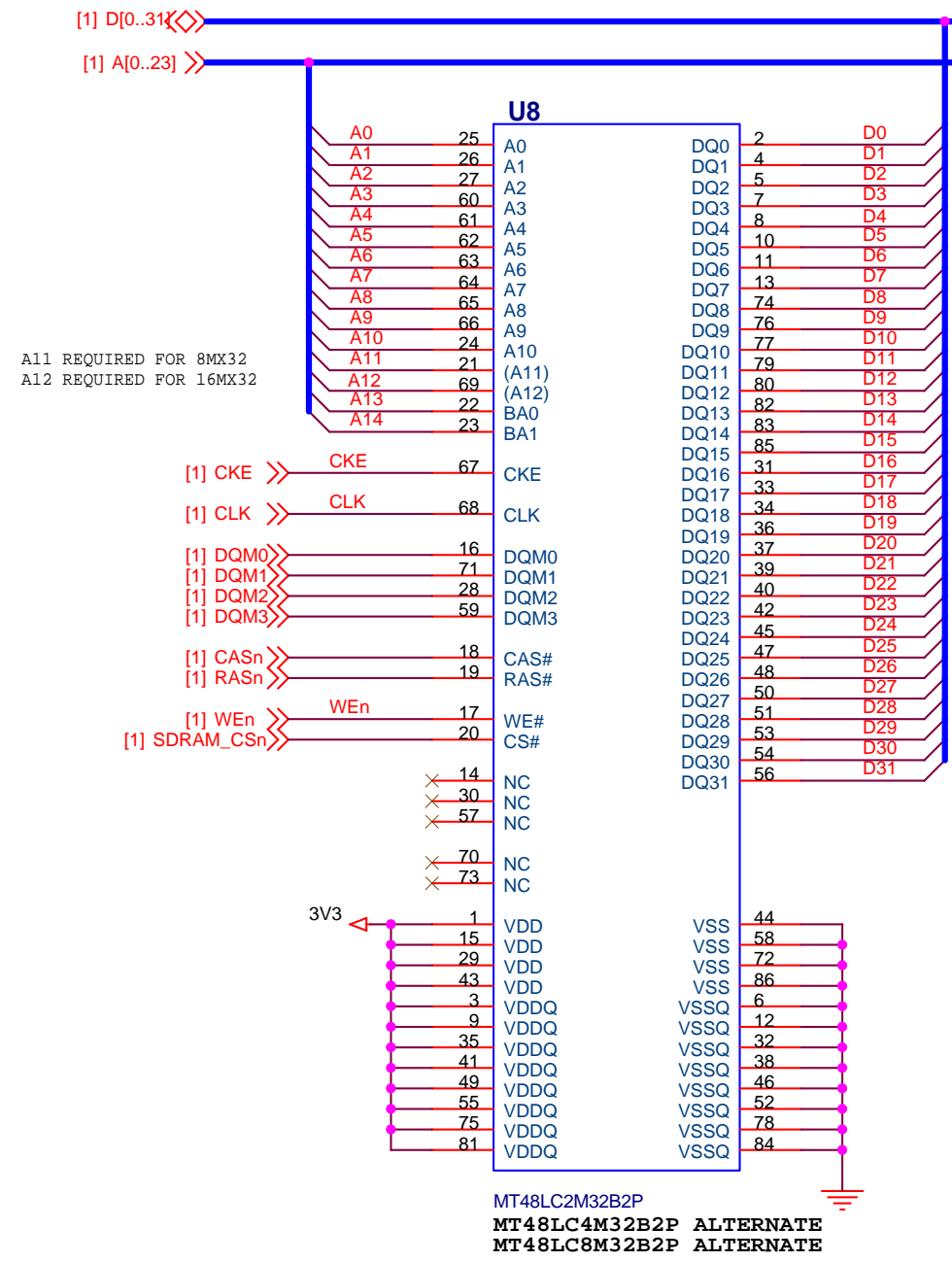
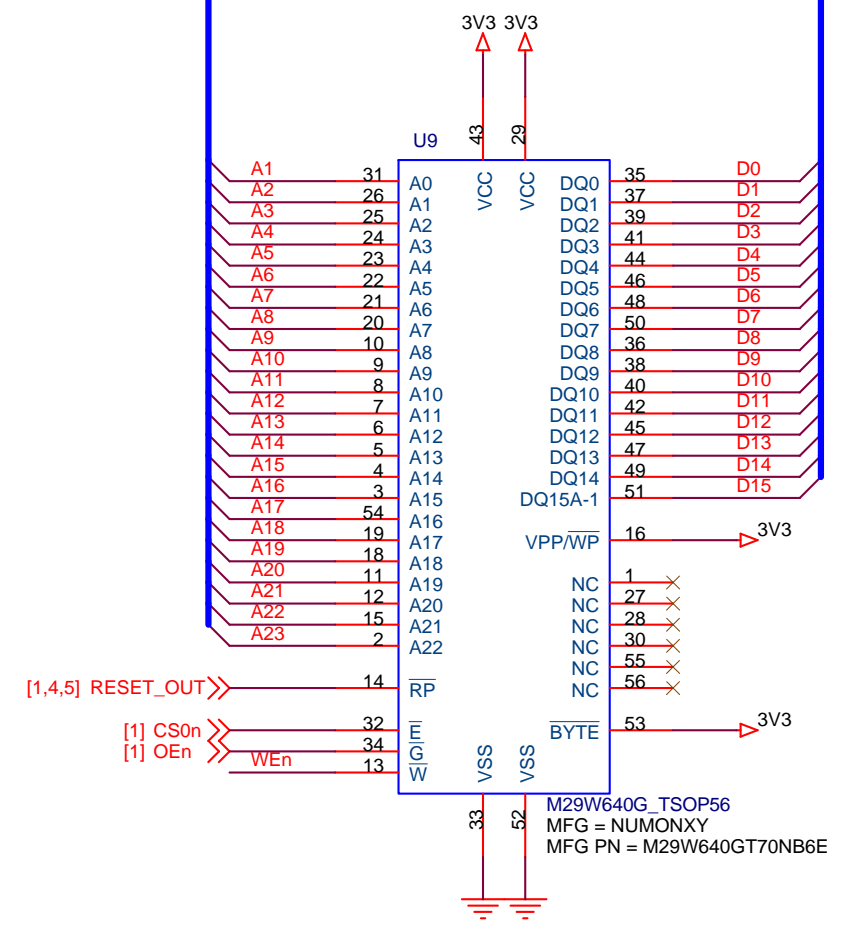
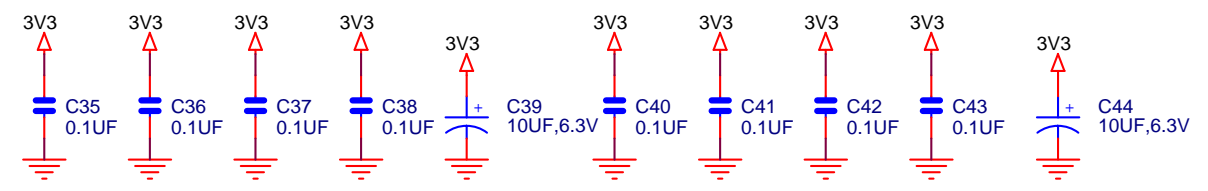


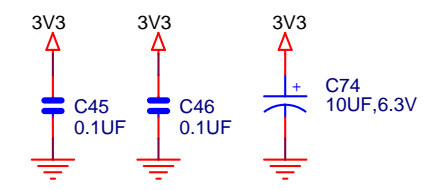
Figure 2. 3-Lead SOT Pin Configuration



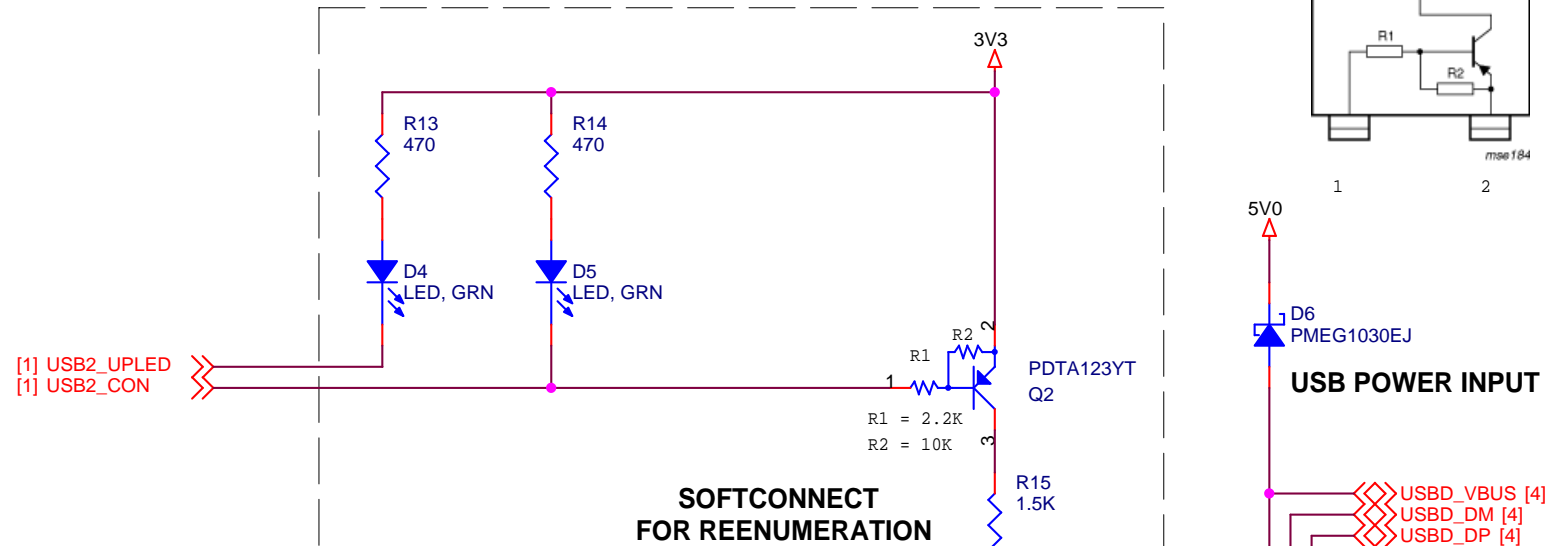
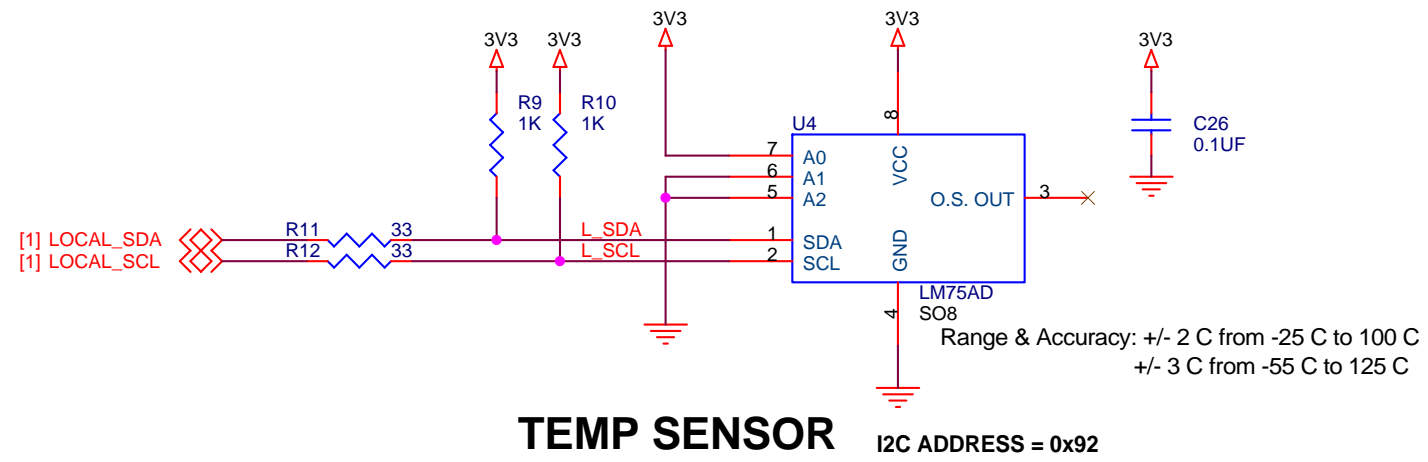
SDRAM (8MB TO 32MB)



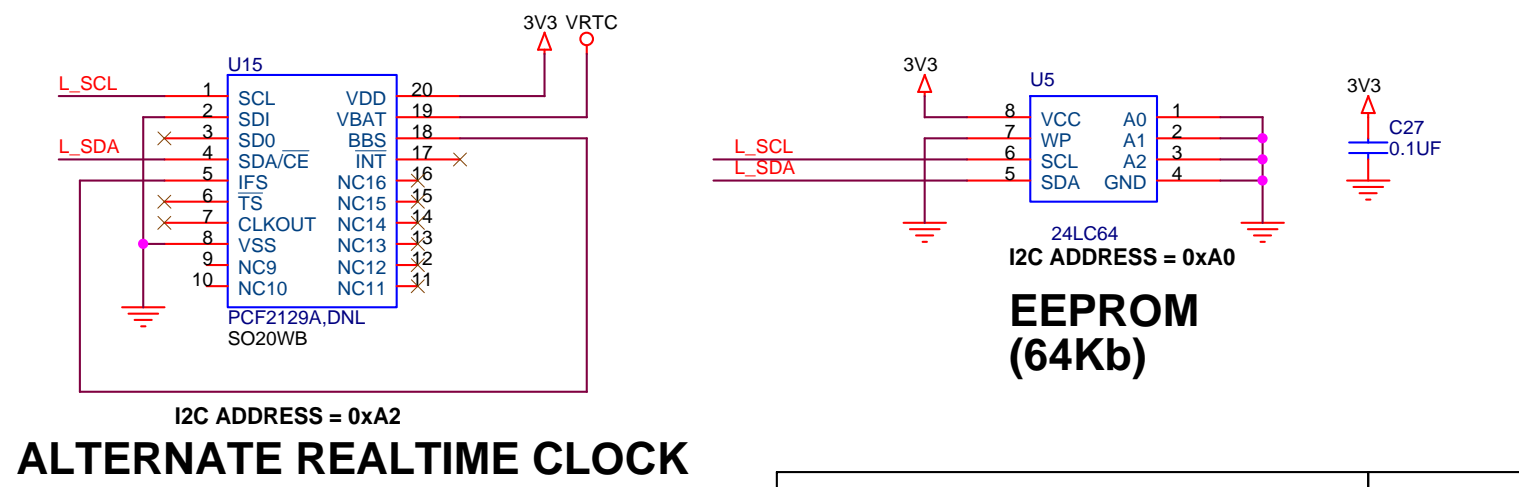
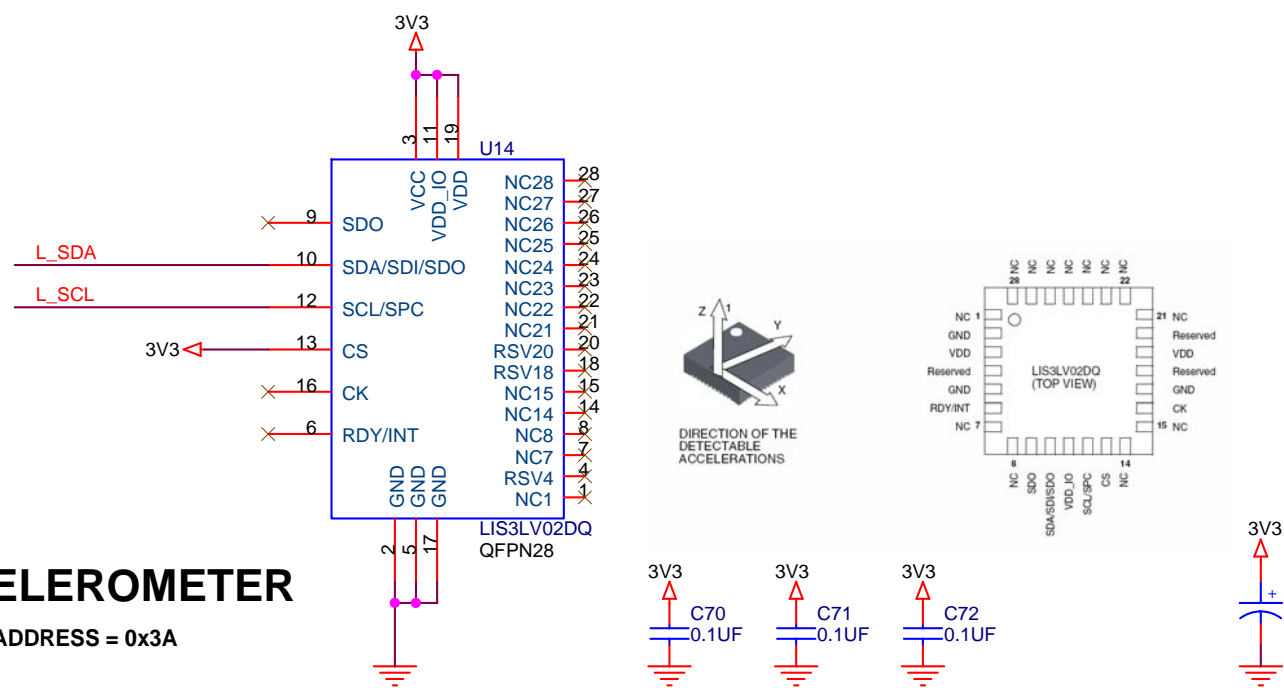
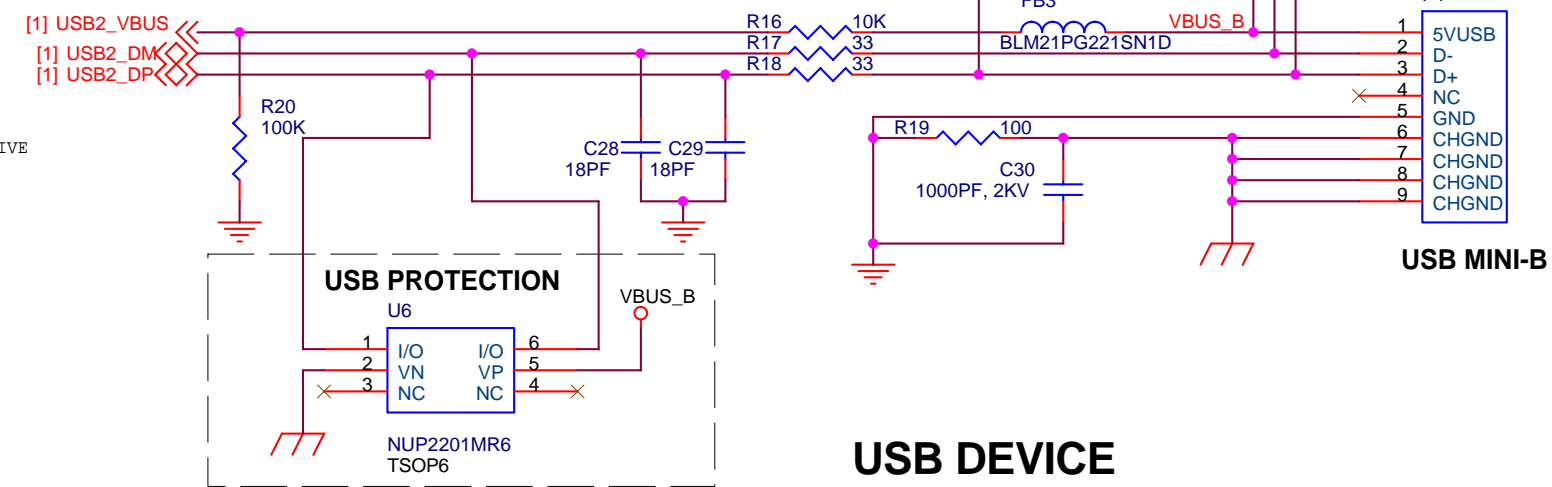
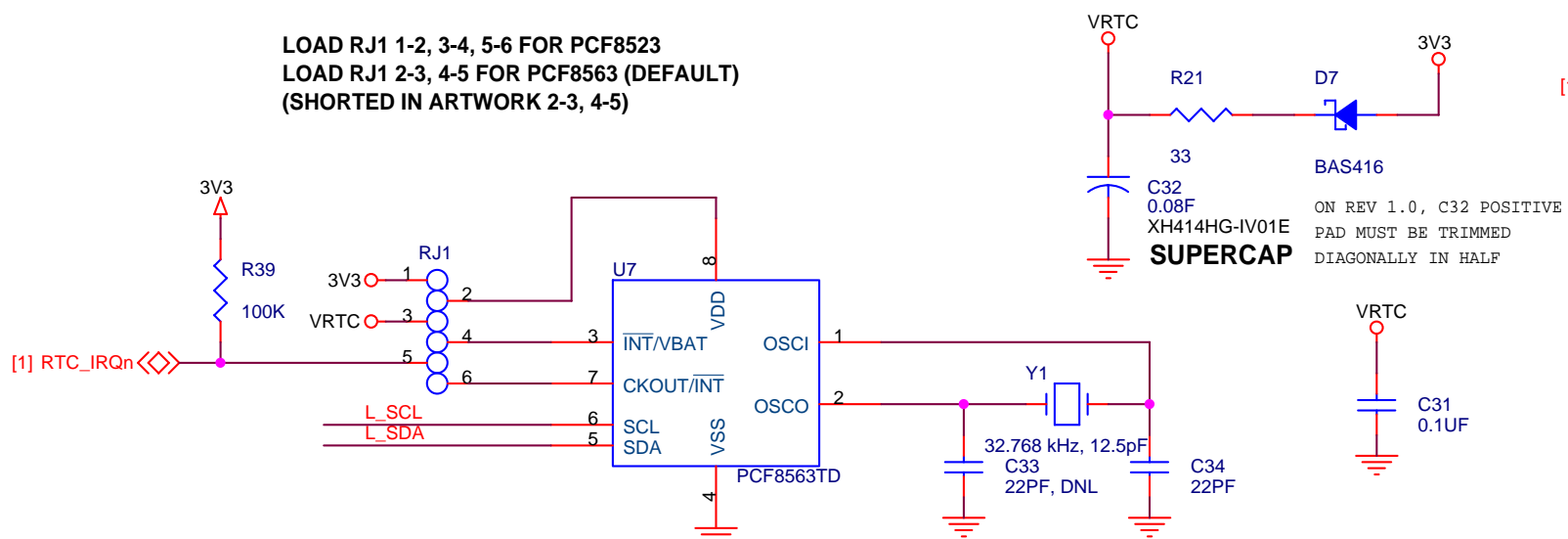
NOR FLASH (8MB TO 16MB)



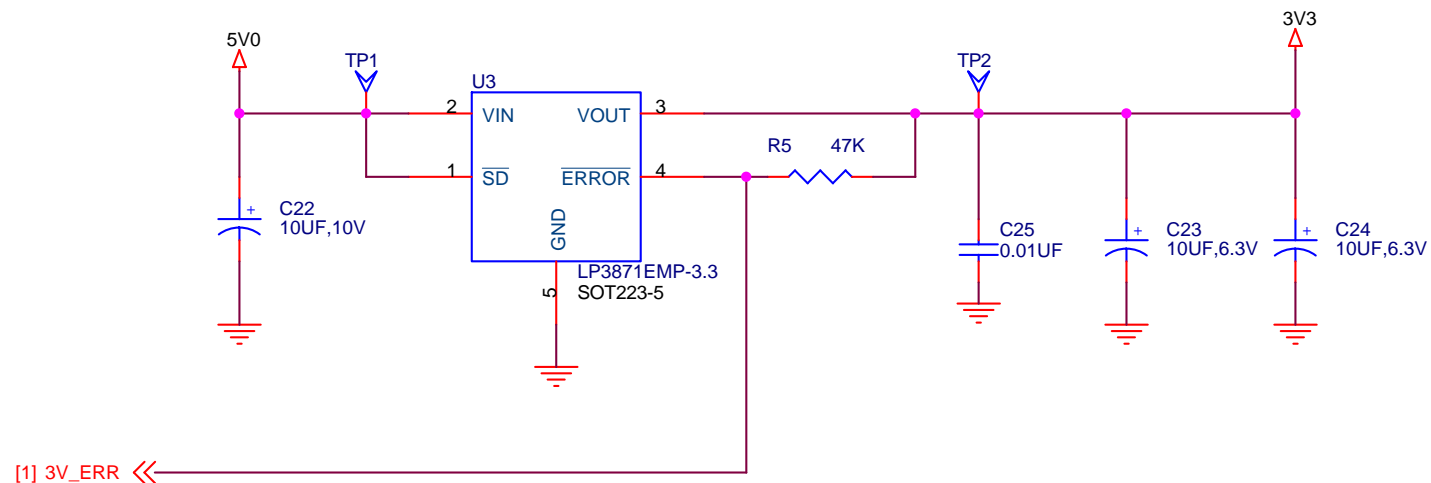
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Title: uEZ GUI 4.3"		
Size B	Document Number	Rev 1.1
Date: Monday, May 10, 2010	Sheet 2 of 5	



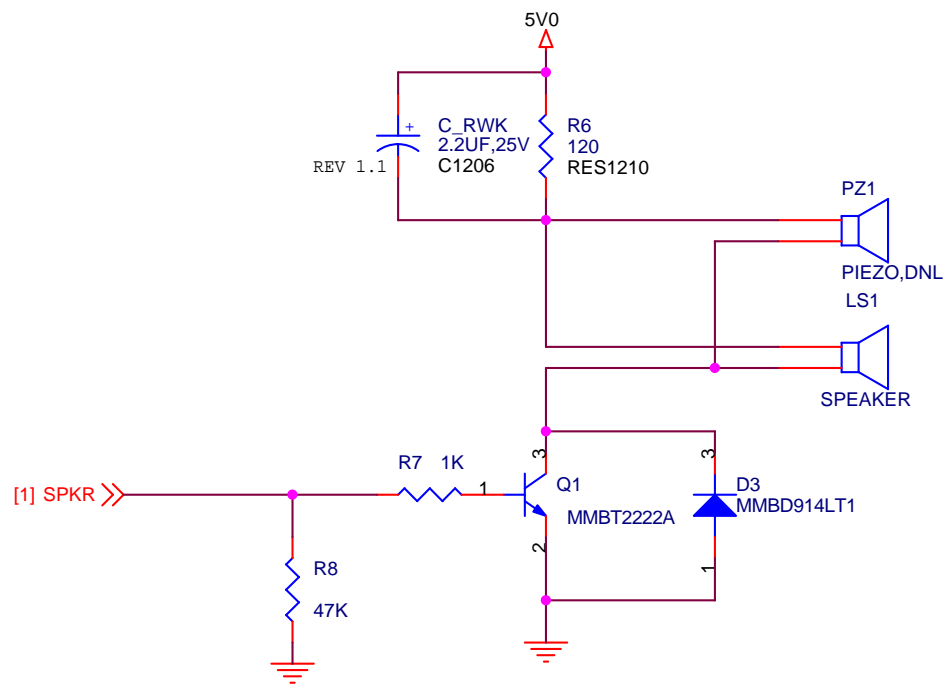
LOAD RJ1 1-2, 3-4, 5-6 FOR PCF8523
LOAD RJ1 2-3, 4-5 FOR PCF8563 (DEFAULT)
(SHORTED IN ARTWORK 2-3, 4-5)



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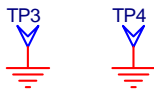
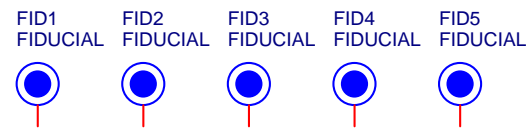
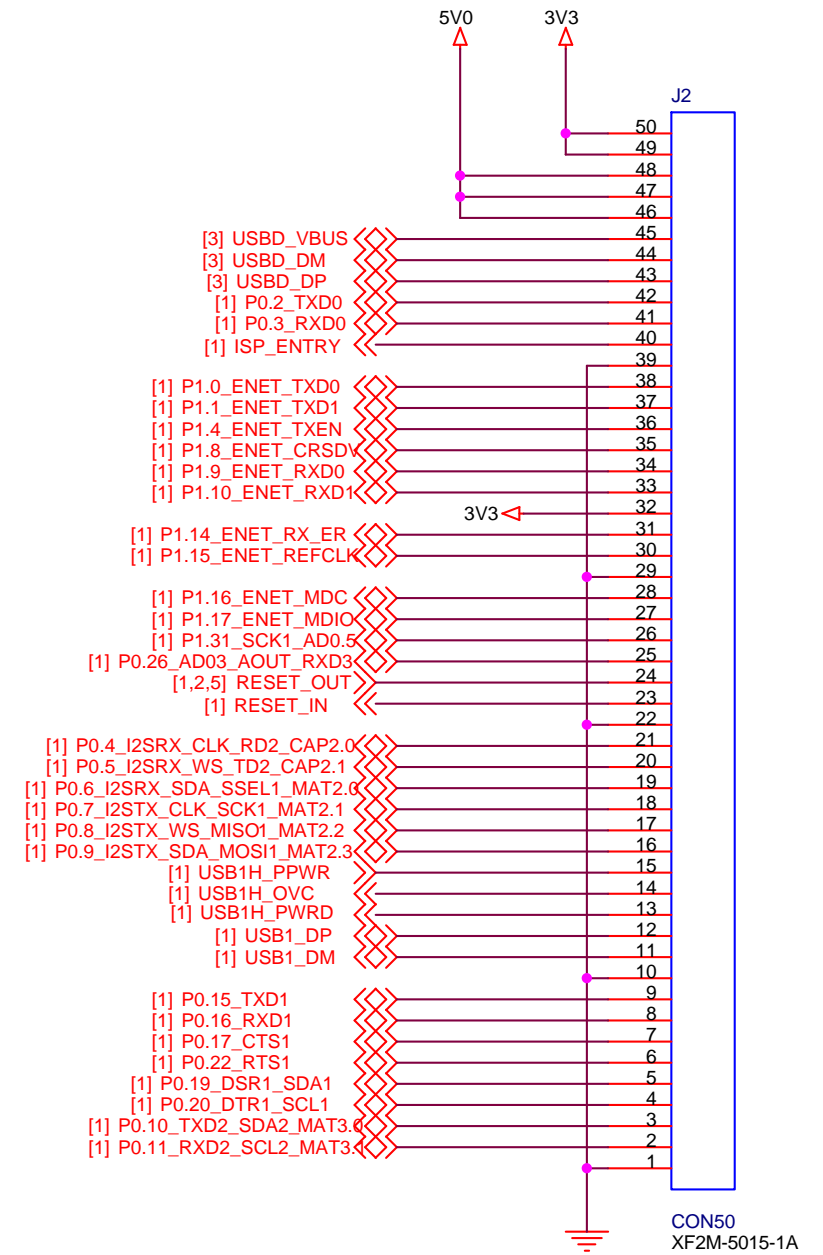


3.3V REGULATOR, 0.8A



SPEAKER/PIEZO

16 OHM, 2.4KHZ



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