

FDI	ENGINEERING CHANGE ORDER		ECO NO: 10-065
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TITLE	DWG NO.	CURRENT REV	NEW REV
uEZ GUI 7	uEZ GUI 7.0 Seiko	1.1	1.2

BACKGROUND:

The Seiko LCD requires that power be cycled whenever the interface signals are 'interrupted', such as a hardware reset of the board. If the power is not cycled, the LCD will flicker when enabled after the reset has occurred.

This rework adds a P-channel FET to control the power to the LCD.

DESCRIPTION OF CHANGE:

1. On the top side, cut the traces going to the power vias for the following connections;
 - a. C68 power (side closest to J7)
 - b. C69 power (side closest to J7)
 - c. C70 power (side closest to J7)
 - d. J7 pin 4,5,6, & 7
2. On the top side, add FET PN: FDN358P to the power via adjacent to C69, loaded up-side-down with the drain connected to the via and the source connected to C69
3. On the top side, add a wire to connect the following pins together in a bus; J7 pin 7, 6, 5, & 4 to C69 +, C68 +, and C70 +
4. On the top side, add a wire from the gate pin of the added FET (FDN358P) to pin U1 pin 193.

Reason for change: 1. Add MOSFET to control power to LCD module		Disposition/Affectivity	
		Use as is	
		Rework to ECO	X
		Scrap & Rebuild	
		Record Change Only	
Prepared by:	MH	Date:	10/21/2010
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