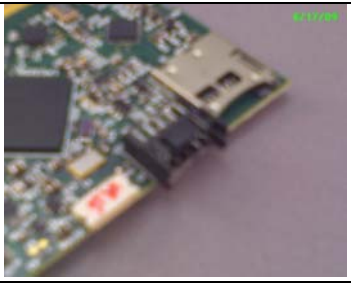


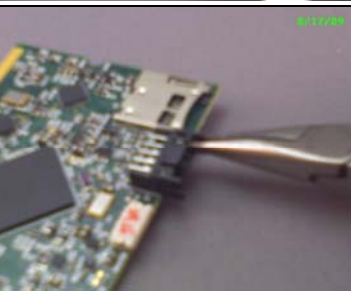
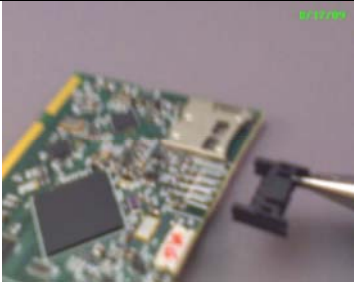
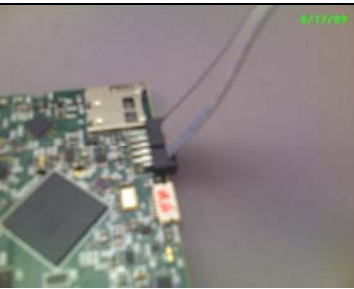


The Segger JTAG Debugger, JLink Lite, uses a connector that is not compatible with the shrouded Mini-JTAG connector (J3) on the current revisions of the ARM7DIMM-LPC2478 (Rev 2.3 and all previous revisions). As a result, the JLink Lite debugger will not plug into the DIMM module properly to allow software debug. There is an easy fix that can be done by simply removing the shrouded plastic piece of the connector J3 from the DIMM module.

The following procedure illustrates how to remove the plastic shroud from the JTAG connector:

<u>Step</u>	<u>Illustration</u>
1) Identify JTAG connector J3, adjacent to the Micro-SD Slot on the DIMM	
2) Using needle-nose pliers, pull on each side of the connector gently, starting with the side adjacent to the Micro-SD	
3) Pull evenly on the opposite side	
4) Work back and forth, alternating between sides, to slowly pull the plastic shroud from the connector. Each pull should be no more than ~0.01".	

<p>5) Be careful when the shroud comes apart from the connector to not bend the pins soldered in the PCB. If any pins get bent, straighten them out prior to attaching the JTAG cable.</p>	 A close-up photograph of the ARM7DIMM PCB connector area. A black plastic shroud is being lifted away from the connector pins. The pins are soldered to the PCB. A green timestamp '12/15/20' is visible in the top right corner of the image.
<p>6) Attach the JTAG cable as shown, with the black stripe closest to the Micro-SD connector.</p>	 A close-up photograph of the ARM7DIMM PCB with the JTAG cable attached. The cable is plugged into the connector. The black stripe on the cable is positioned closest to the Micro-SD connector. A green timestamp '12/15/20' is visible in the top right corner of the image.