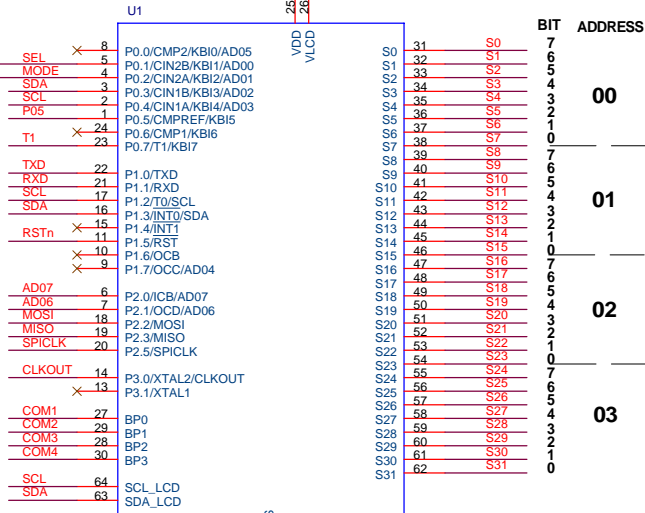
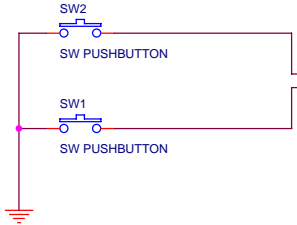
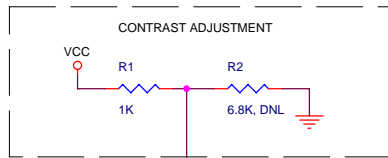


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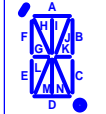
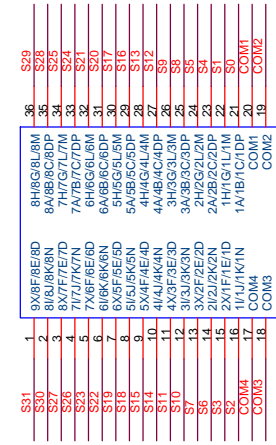
Page	Description
1	THIS PAGE
2	LCD DEMO-SC

Revision History

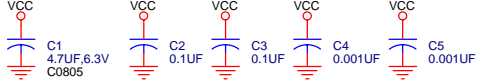
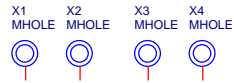
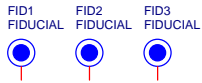
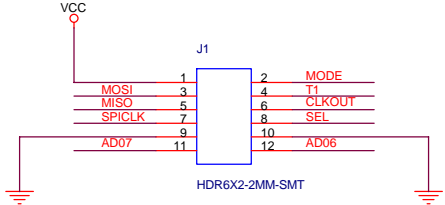
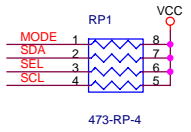
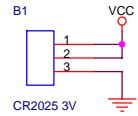
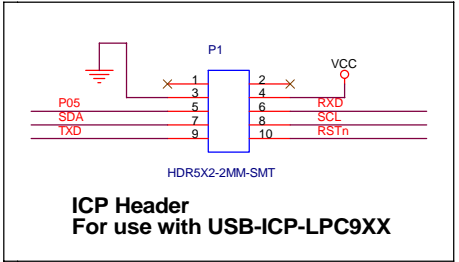
REV	Description
1	ORIGINAL BUILD
2	CORRECTED PINOUT OF J1 AND P1



BIT	ADDRESS
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6	00
5	00
4	00
3	00
2	00
1	00
0	00
7	01
6	01
5	01
4	01
3	01
2	01
1	01
0	01
7	02
6	02
5	02
4	02
3	02
2	02
1	02
0	02
7	03
6	03
5	03
4	03
3	03
2	03
1	03
0	03



LCD1
LCD-8CHAR, PD-878



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